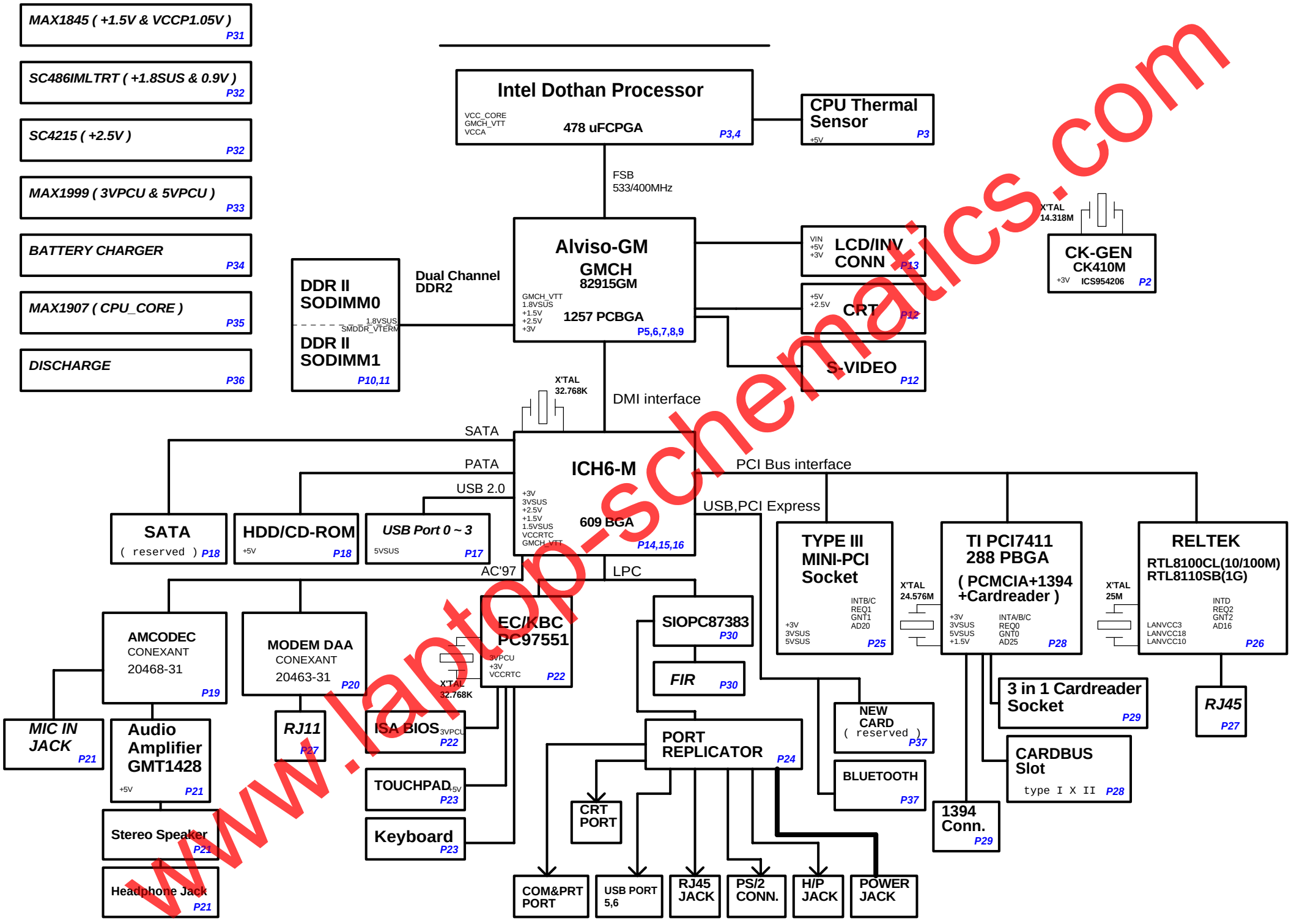

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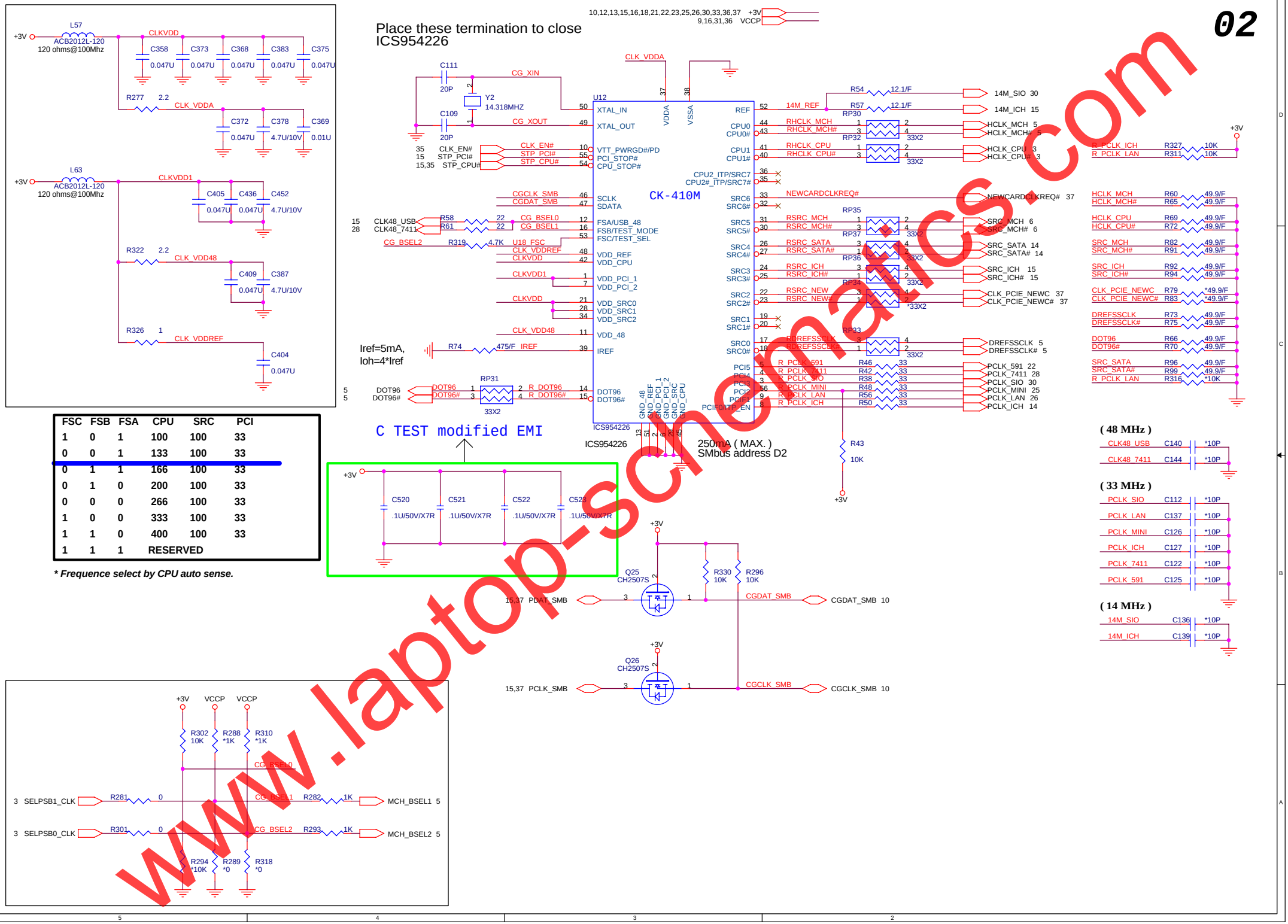
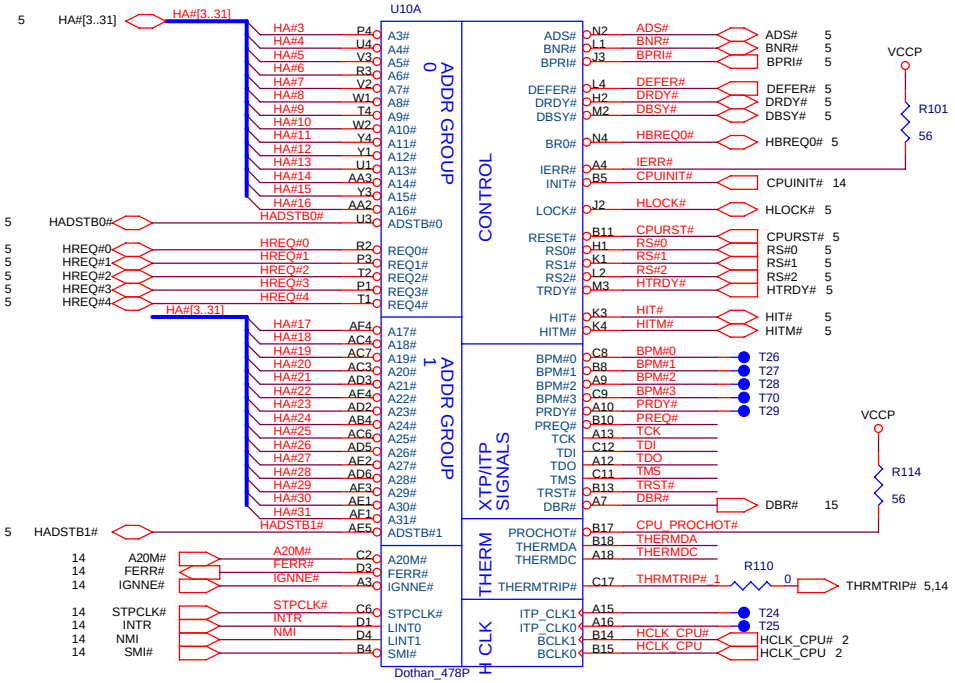
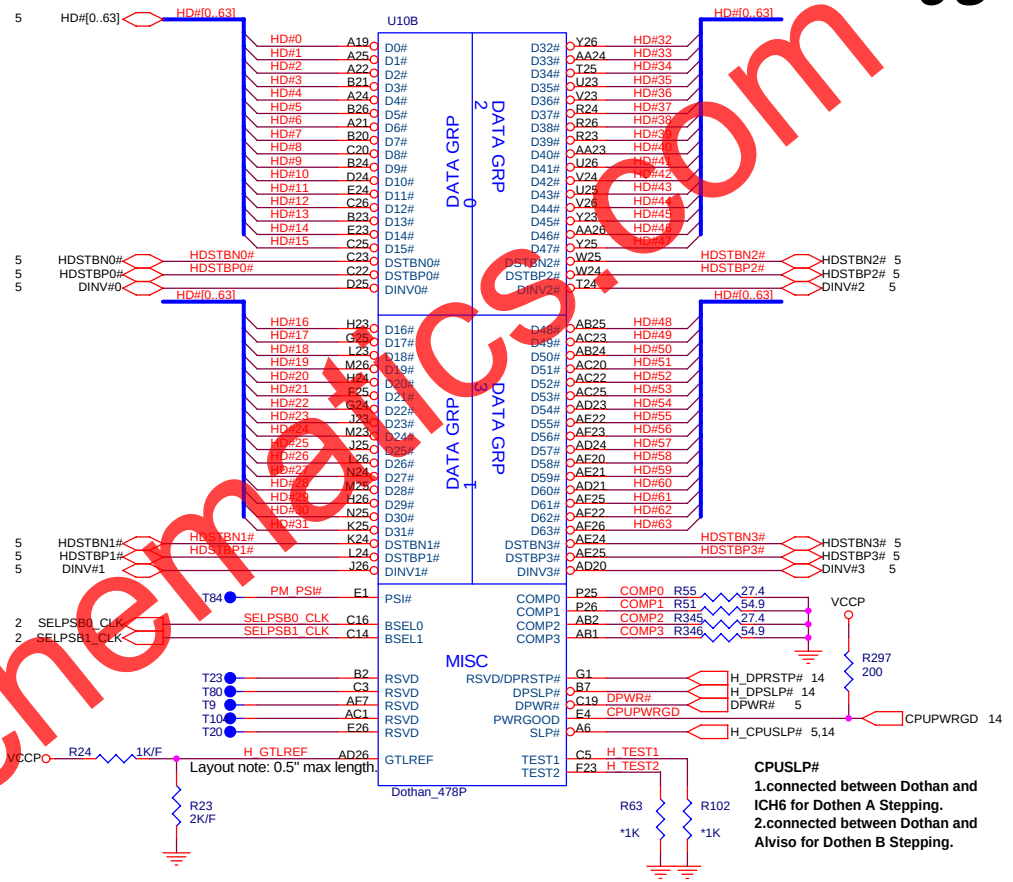
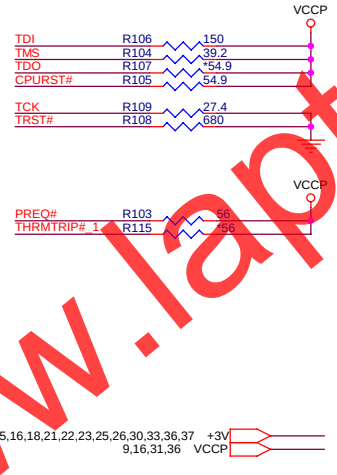
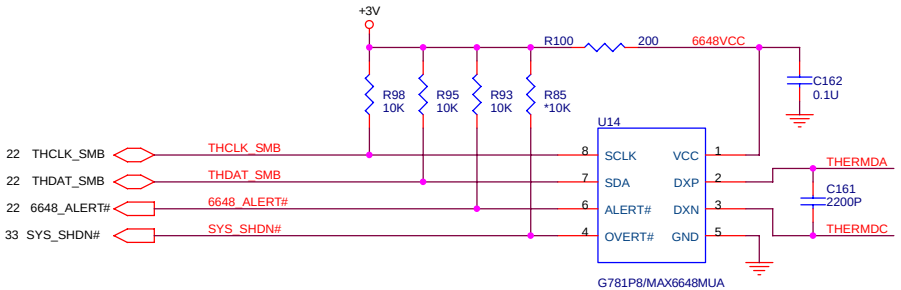


FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	33
0	0	1	133	100	33
0	1	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	RESERVED		









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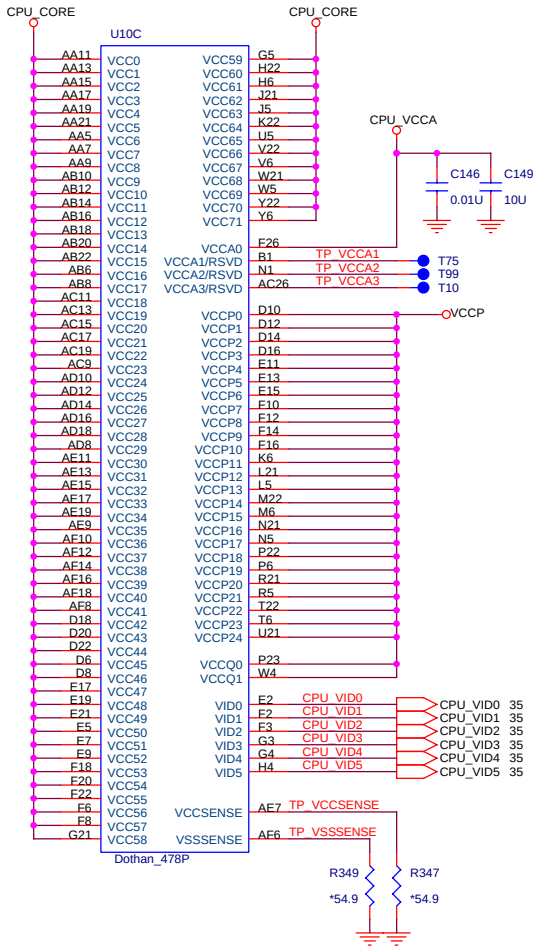

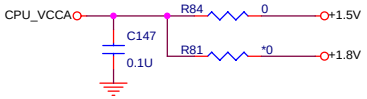

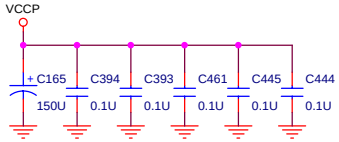

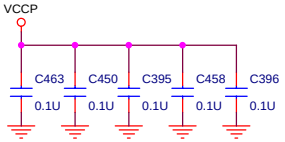

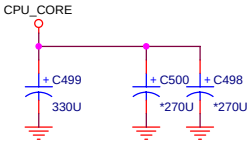

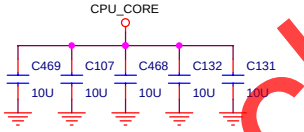

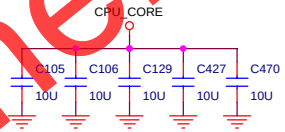

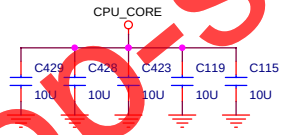

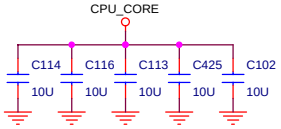

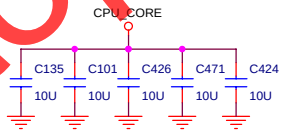

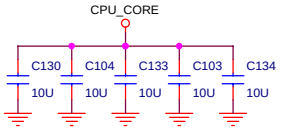

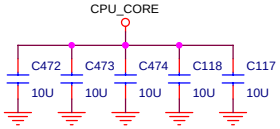

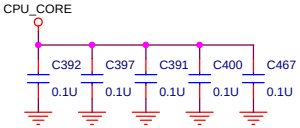

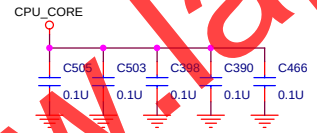

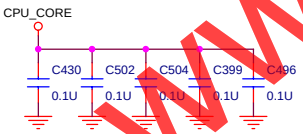

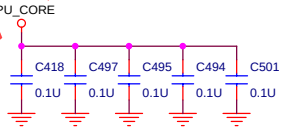

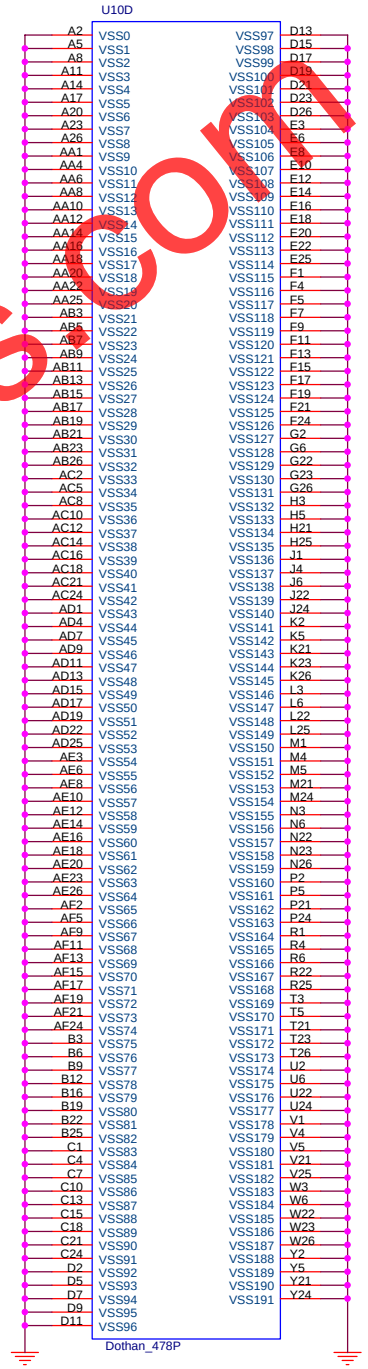

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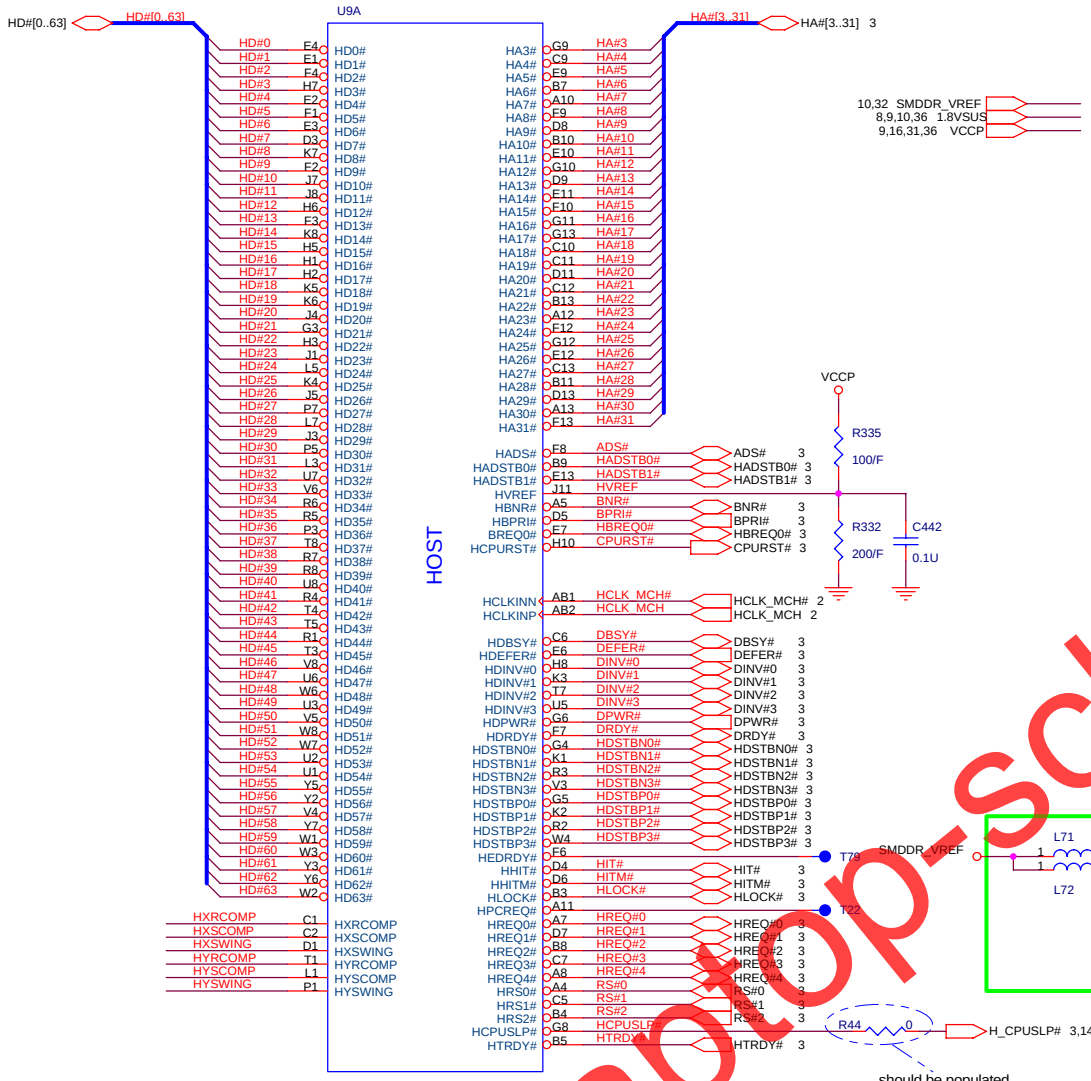
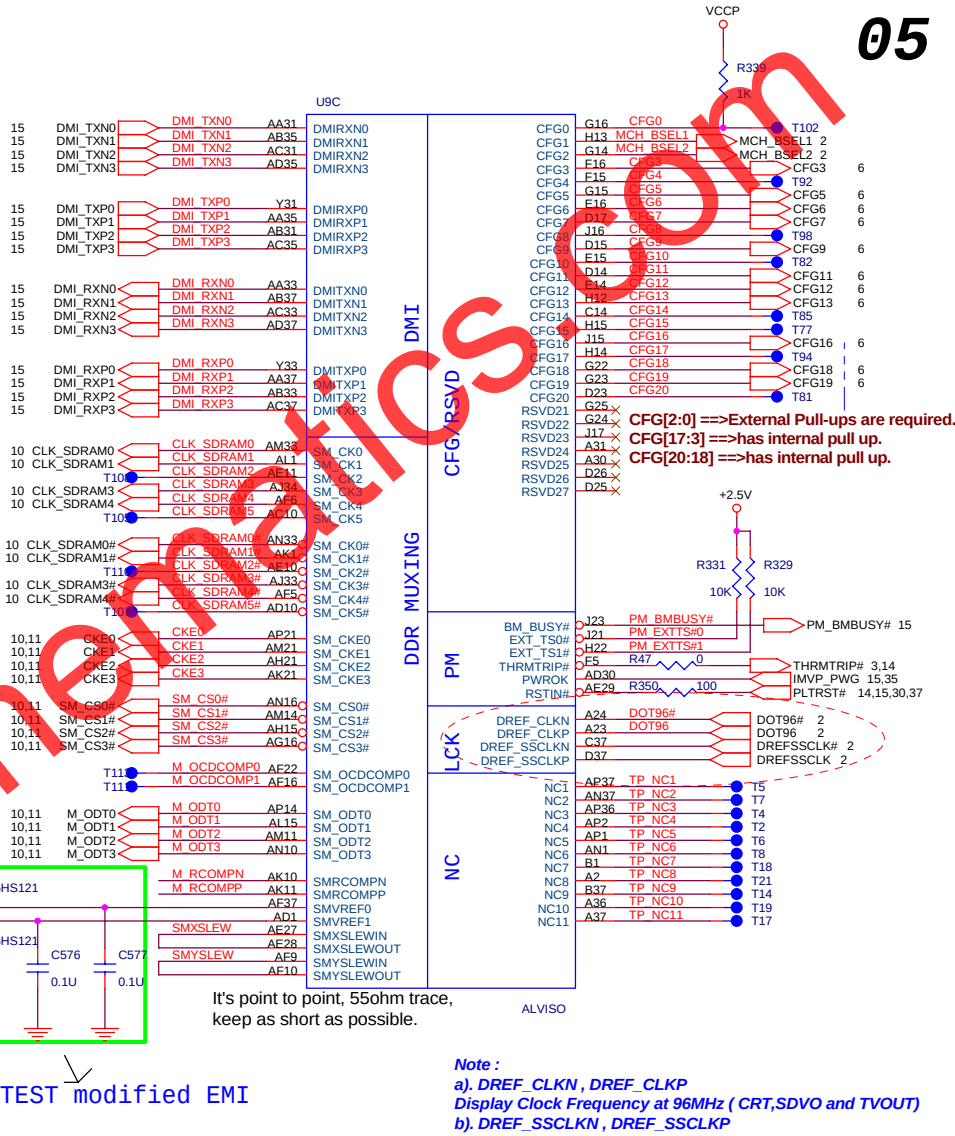
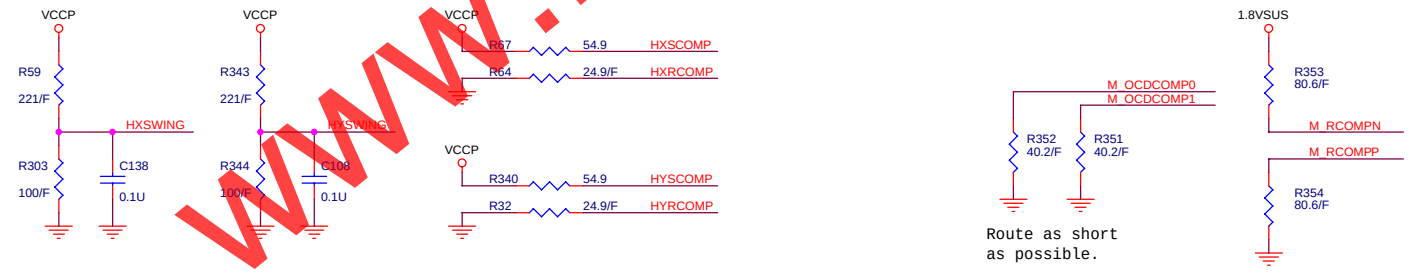




Note :
a). DREF_CLKN , DREF_CLKP
Display Clock Frequency at 96MHz (CRT,SDVO and TVOUT)
b). DREF_SSCLKN , DREF_SSCLKP
Display Clock Frequency (with SSC) at 96,100MHz (LVDS)



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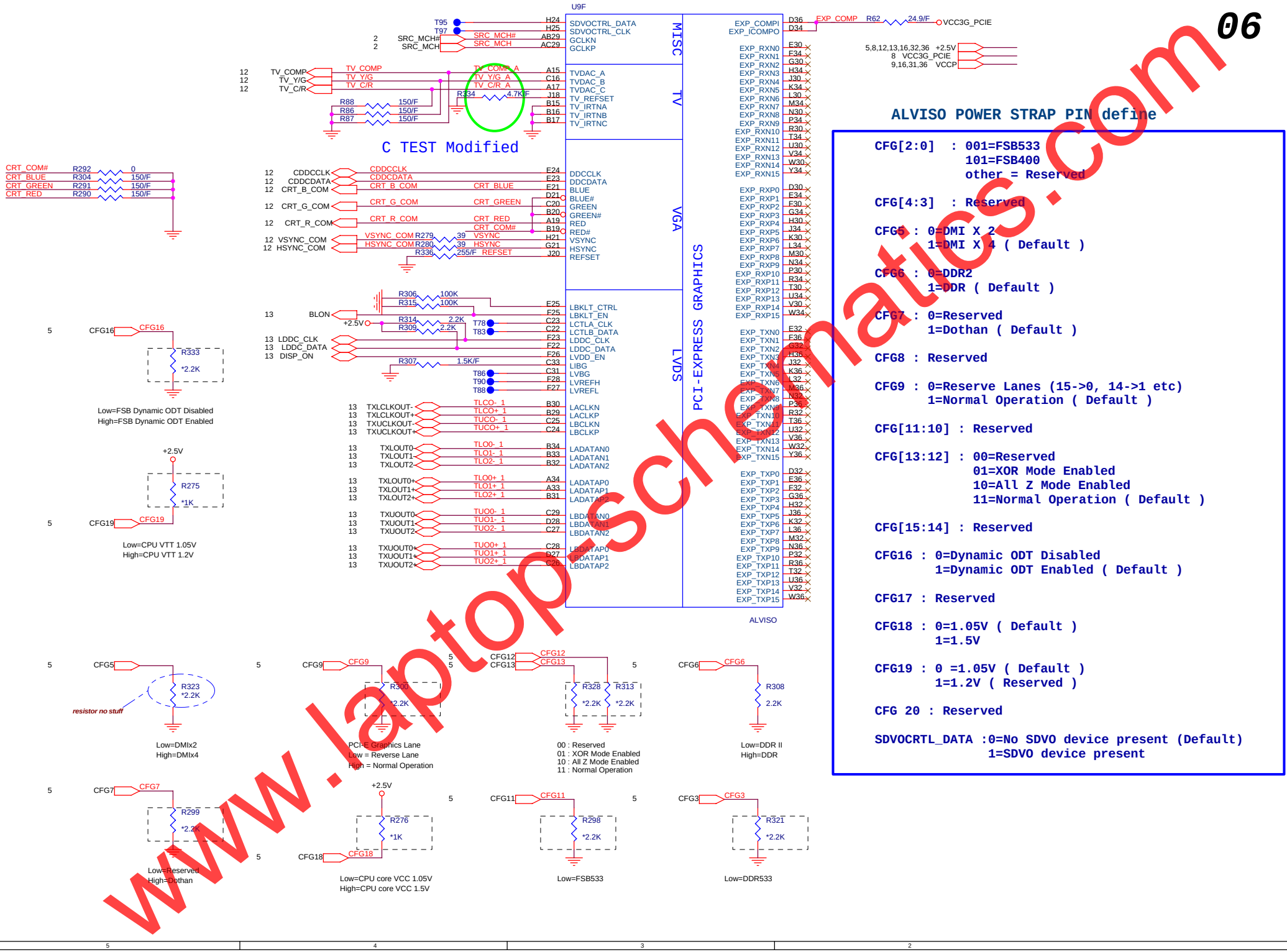

ALVISO POWER STRAP PIN define

CFG[2:0] : 001=FSB533
101=FSB400
other = Reserved

CFG[4:3] : Reserved

CFG5 : 0=DMI X 2
1=DMI X 4 (Default)

CFG6 : 0=DDR2
1=DDR (Default)

CFG7 : 0=Reserved
1=Dothan (Default)

CFG8 : Reserved

CFG9 : 0=Reserve Lanes (15->0, 14->1 etc)
1=Normal Operation (Default)

CFG[11:10] : Reserved

CFG[13:12] : 00=Reserved
01=XOR Mode Enabled
10=All Z Mode Enabled
11=Normal Operation (Default)

CFG[15:14] : Reserved

CFG16 : 0=Dynamic ODT Disabled
1=Dynamic ODT Enabled (Default)

CFG17 : Reserved

CFG18 : 0=1.05V (Default)
1=1.5V

CFG19 : 0 =1.05V (Default)
1=1.2V (Reserved)

CFG 20 : Reserved

SDVOCRTL_DATA :0=No SDVO device present (Default)
1=SDVO device present

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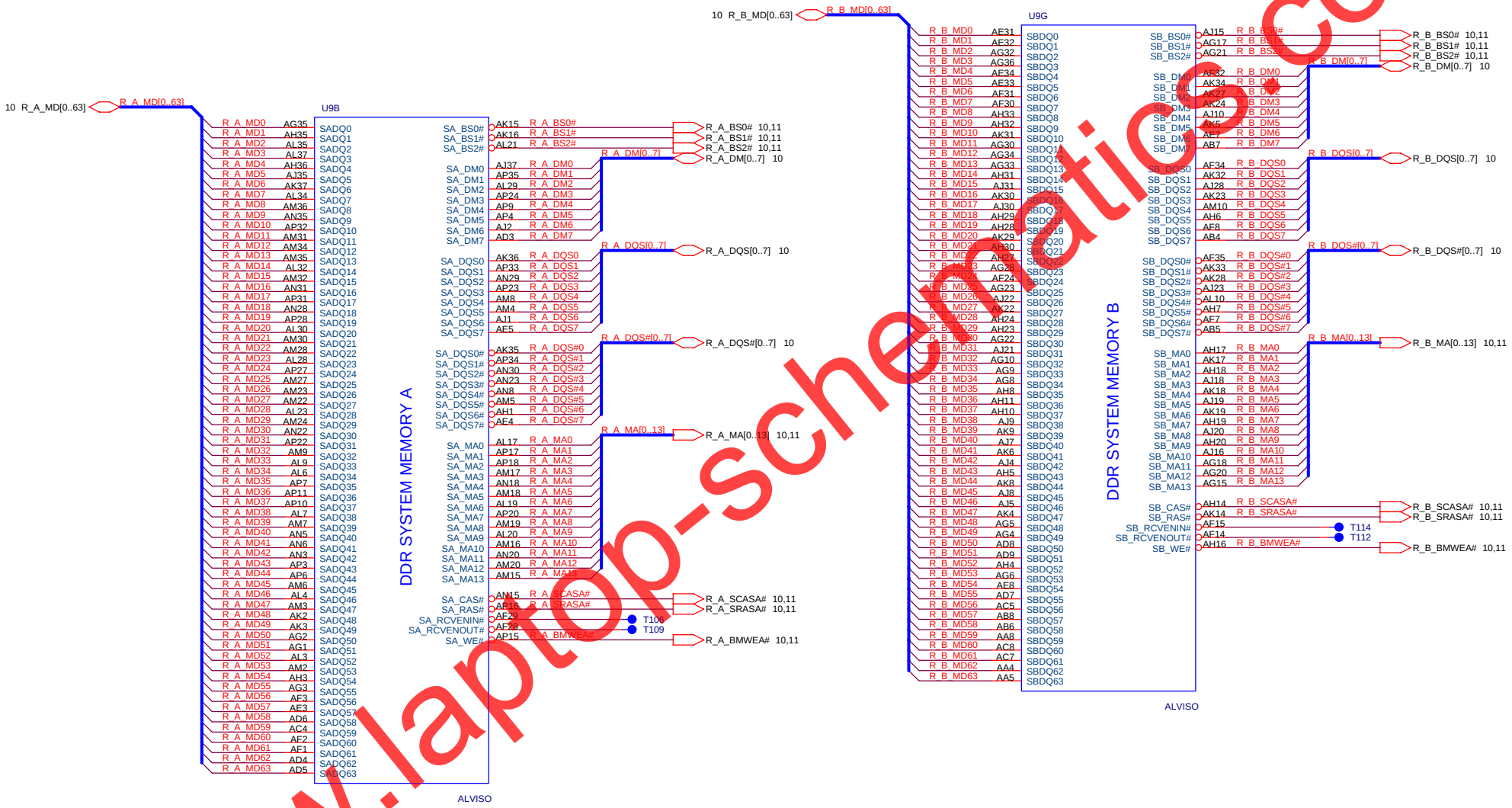

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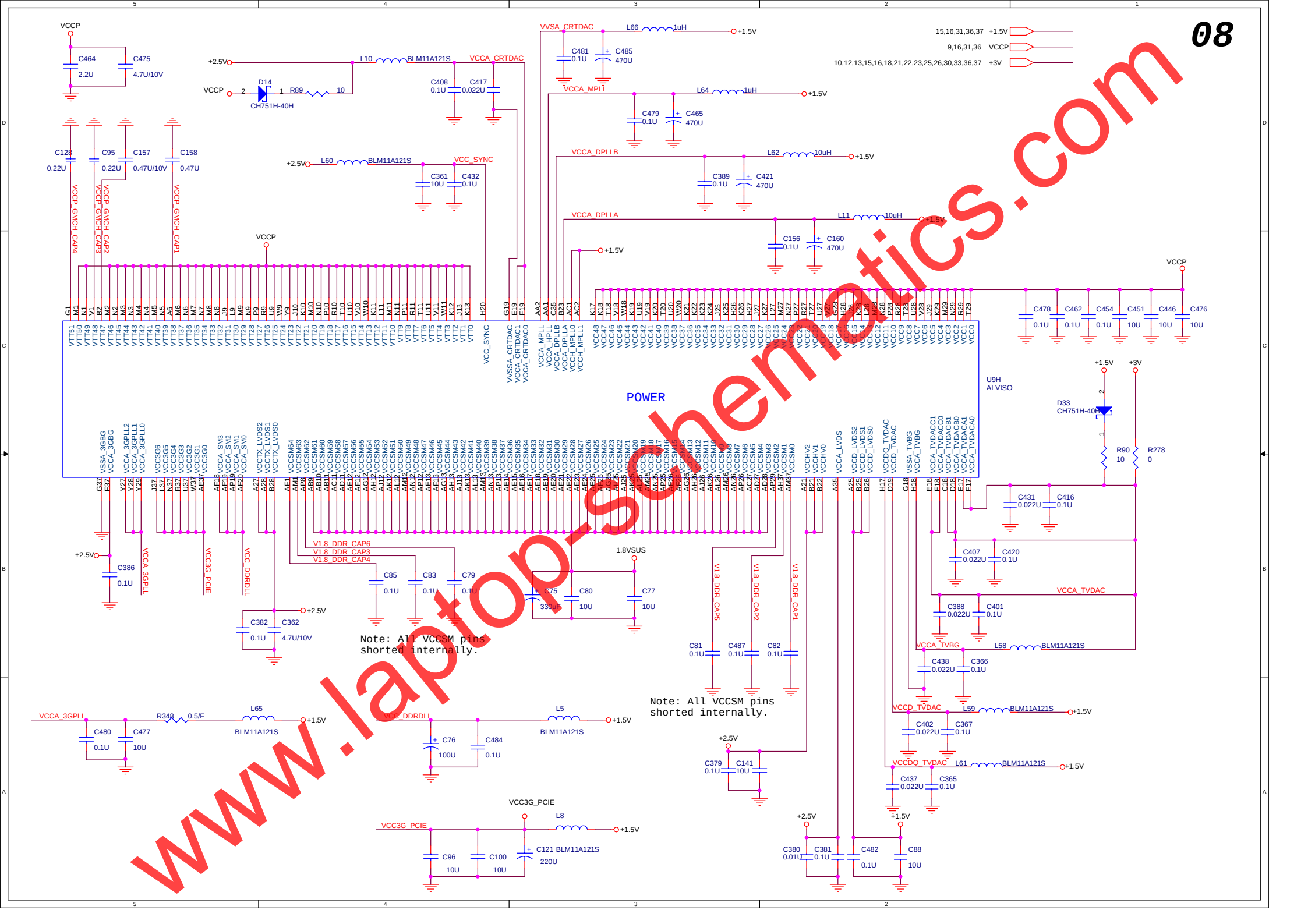
08
15,16,31,36,37 +1.5V
9,16,31,36 VCCP
10,12,13,15,16,18,21,22,23,25,26,30,33,36,37 +3V
POWER
U9H
ALVISO
Note: All VCCSM pins shorted internally.
Note: All VCCSM pins shorted internally.
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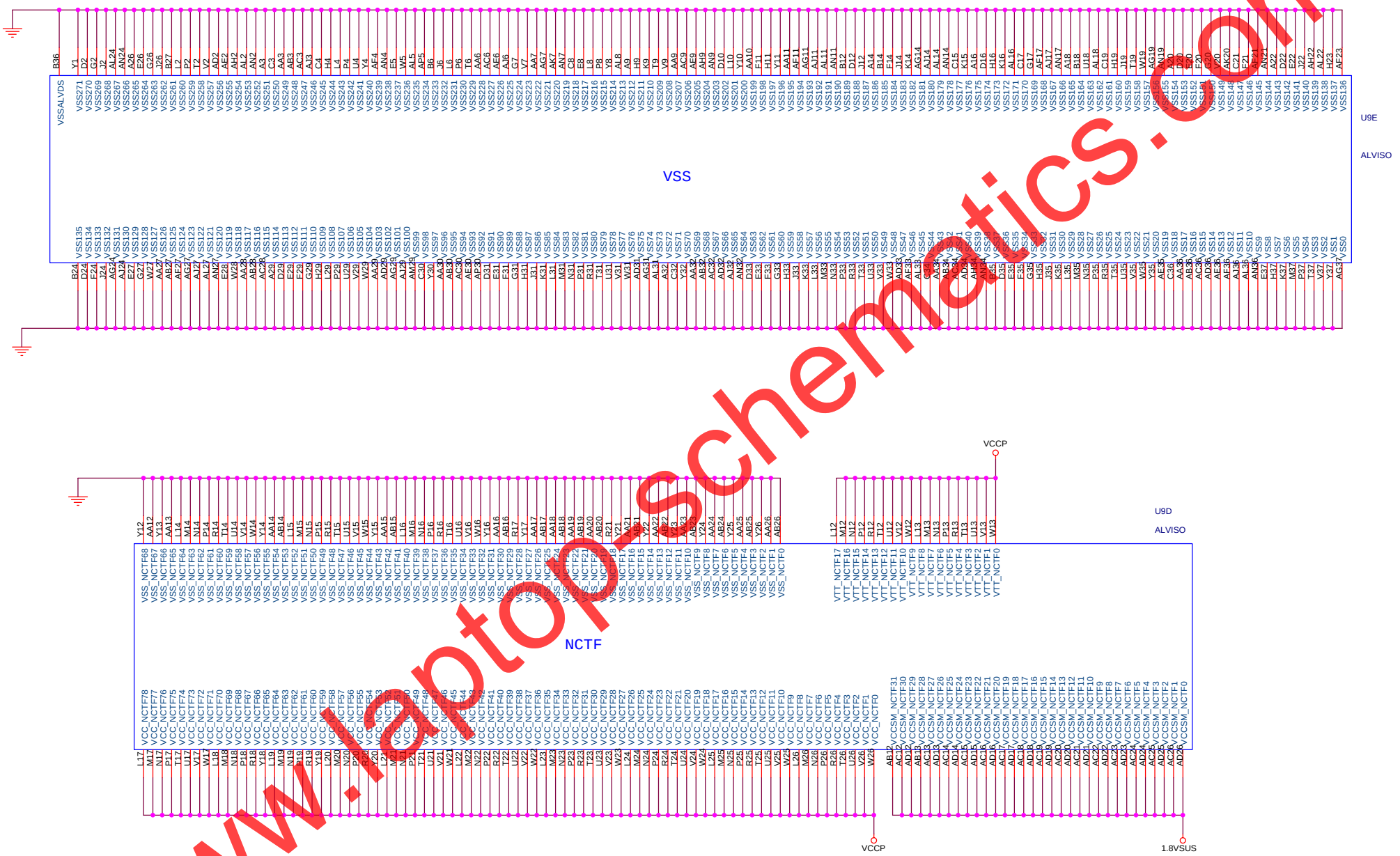

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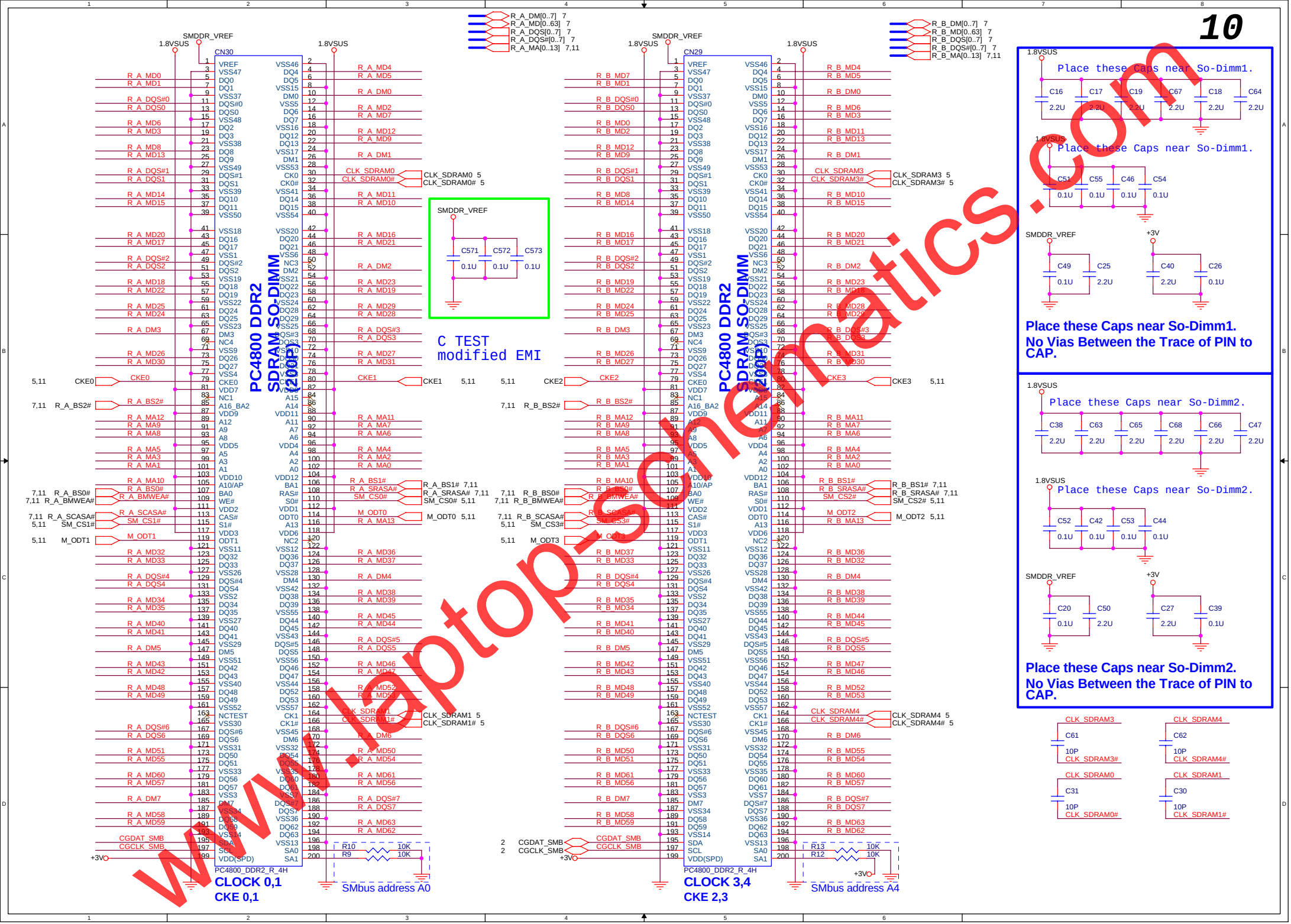
CN30
PC4800 DDR2 SDRAM SO-DIMM
PC4800_DDR2_R_4H
CLOCK 0,1
CKE 0,1
SMbus address A0
CN29
PC4800 DDR2 SDRAM SO-DIMM
PC4800_DDR2_R_4H
CLOCK 3,4
CKE 2,3
SMbus address A4
C TEST
modified EMI
Place these Caps near So-Dimm1.
No Vias Between the Trace of PIN to CAP.
Place these Caps near So-Dimm2.
No Vias Between the Trace of PIN to CAP.
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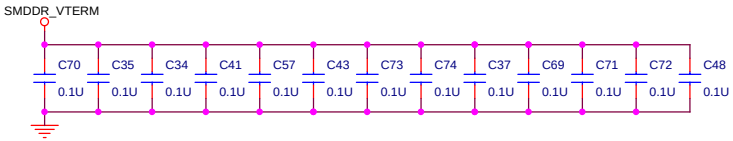
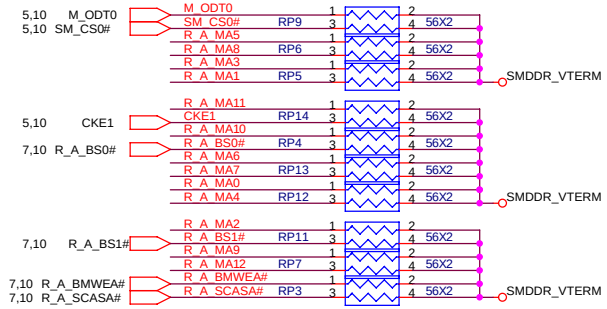
DDRII DUAL CHANNEL A,B.

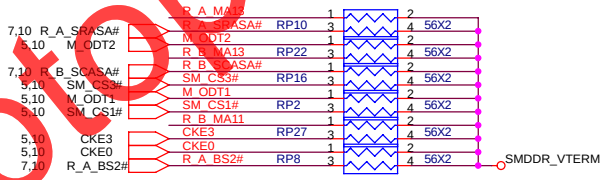
DDRII A CHANNEL

SMDDR_VTERM 32,36

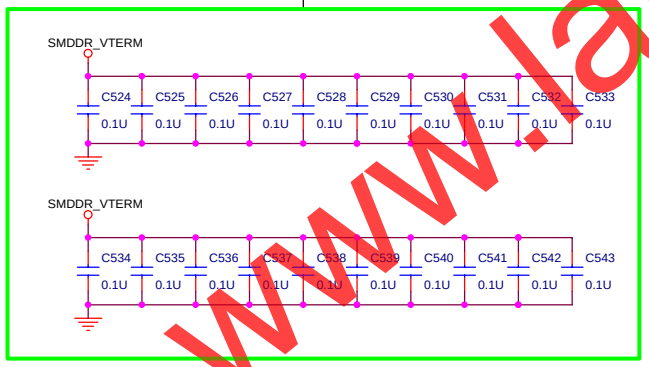
R_A_MA[0..13] 7,10





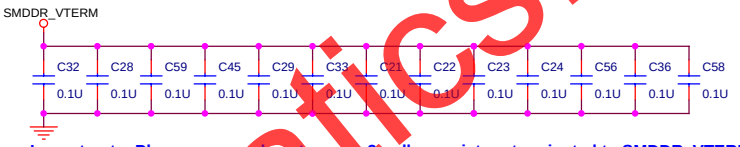


C TEST modified EMI

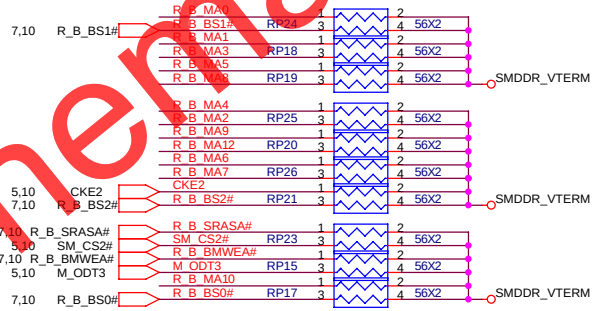


DDRII B CHANNEL

R_B_MA[0..13] 7,10



Layout note: Place one cap close to every 2 pullup resistors terminated to SMDDR_VTERM



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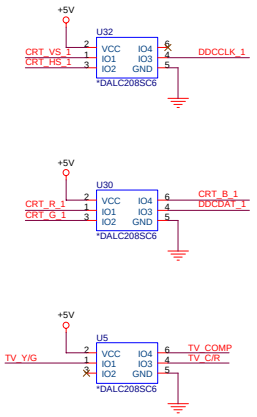
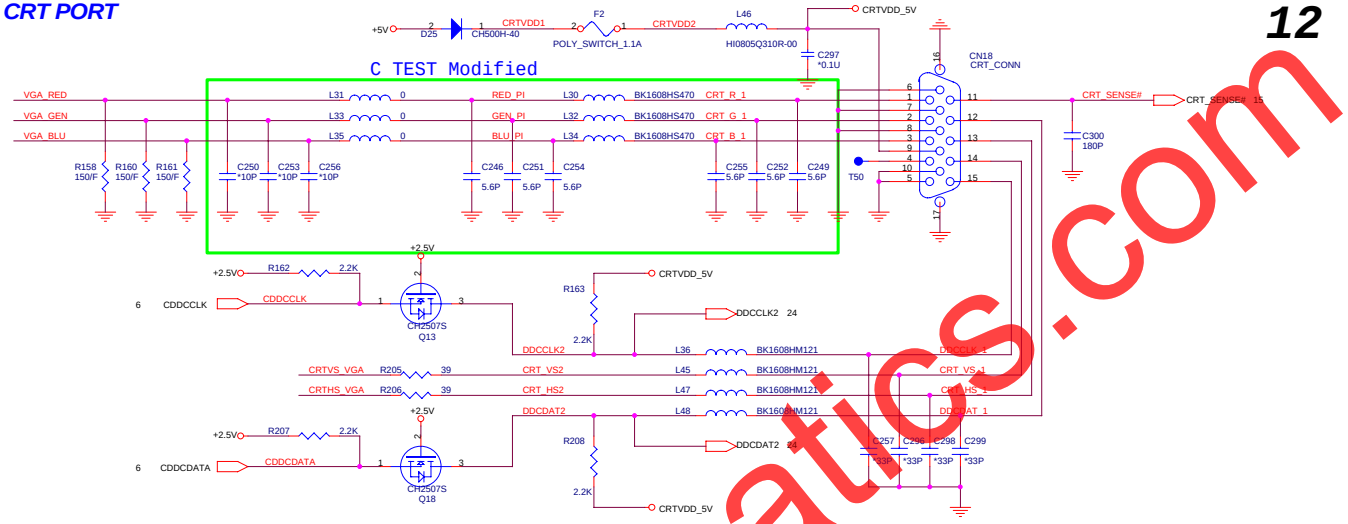
ESD PORTECTION

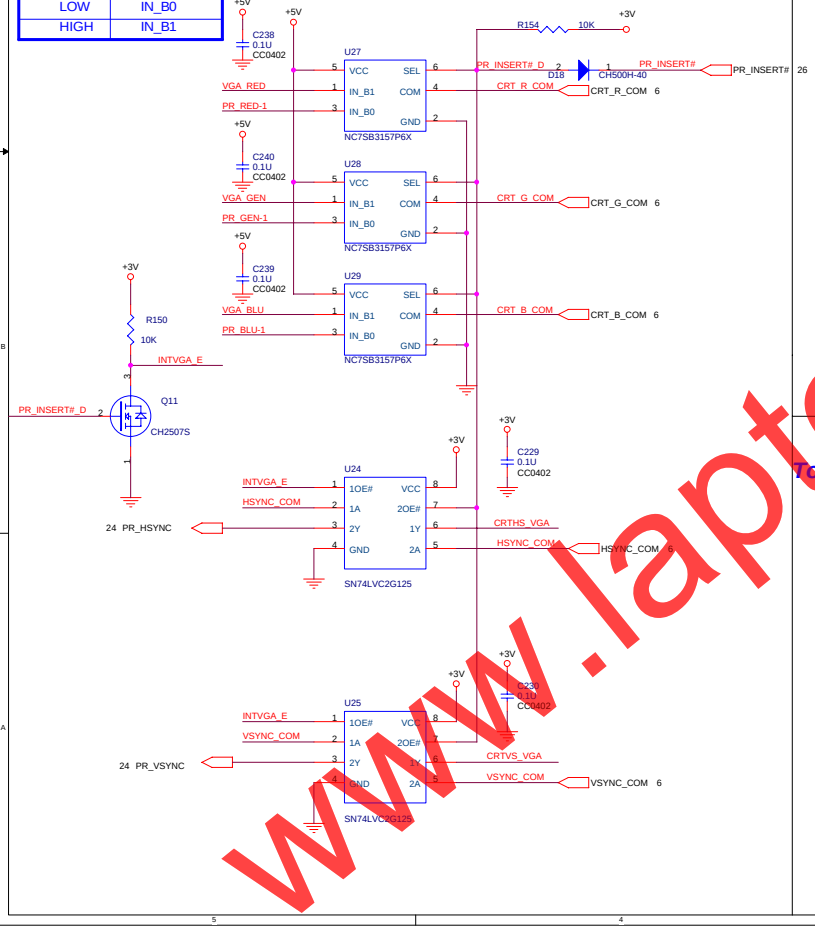


CRT PORT

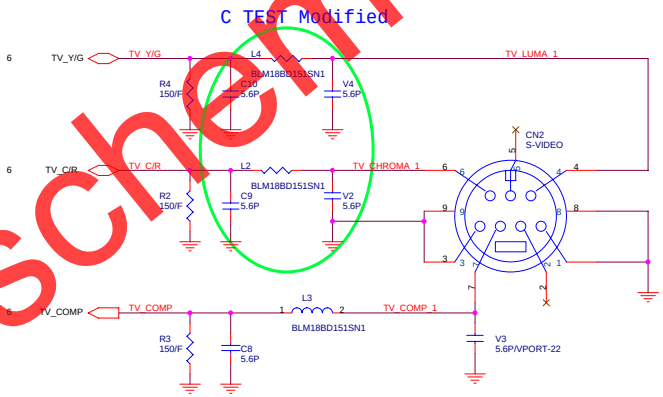


CRT SWITCH

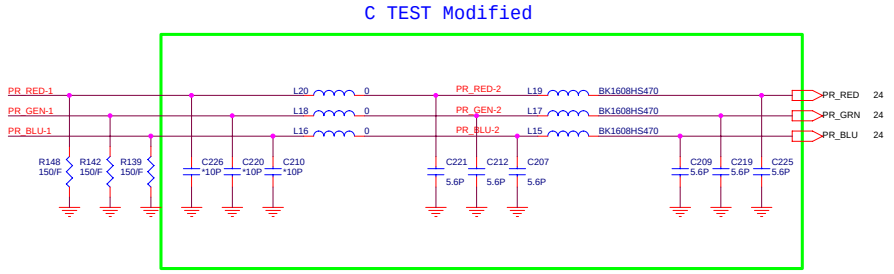
SEL	FUNCTION(COM
LOW	IN_B0
HIGH	IN_B1



TV-OUT



To port - replicator C.R.T



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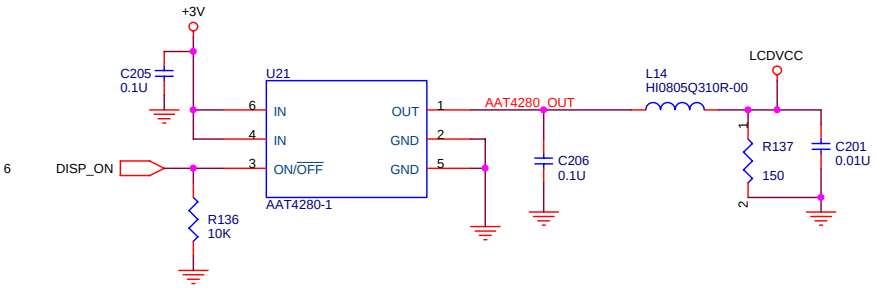
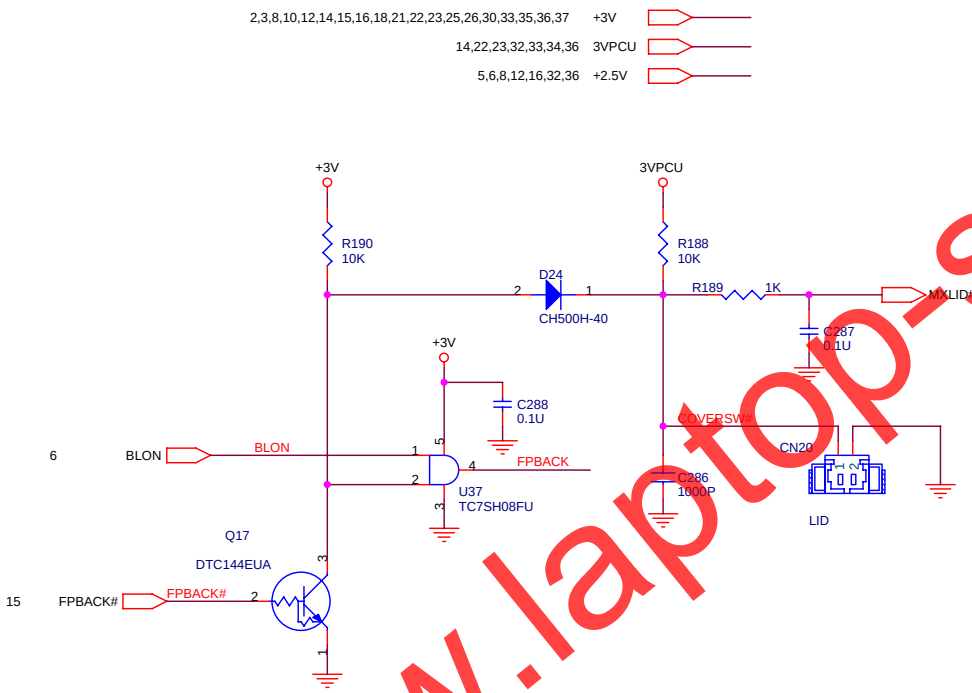
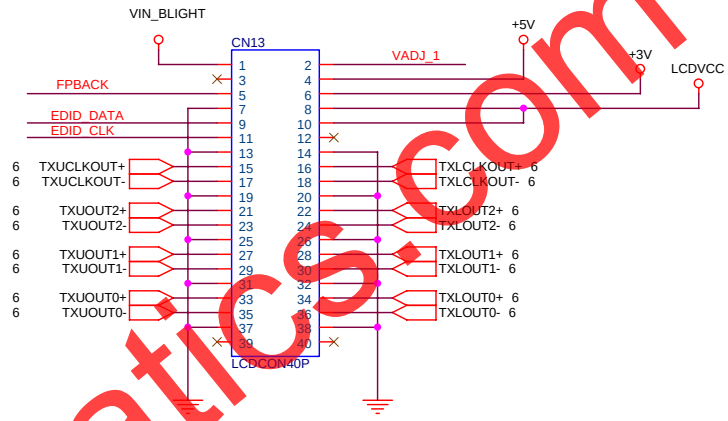
PANEL VCC CONTROL

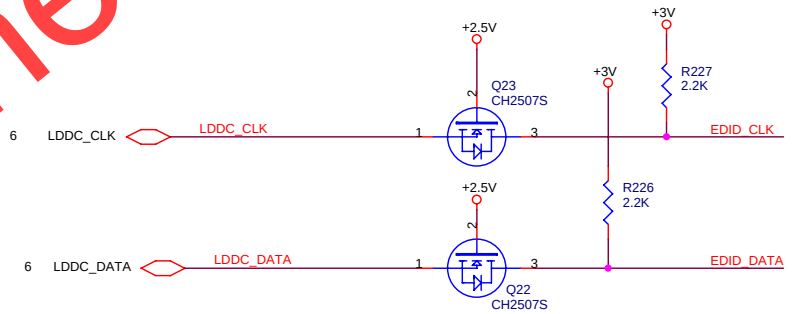


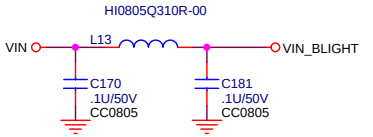
BACKLIGHT CONTROL

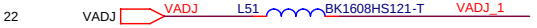


LCD CONNECTOR









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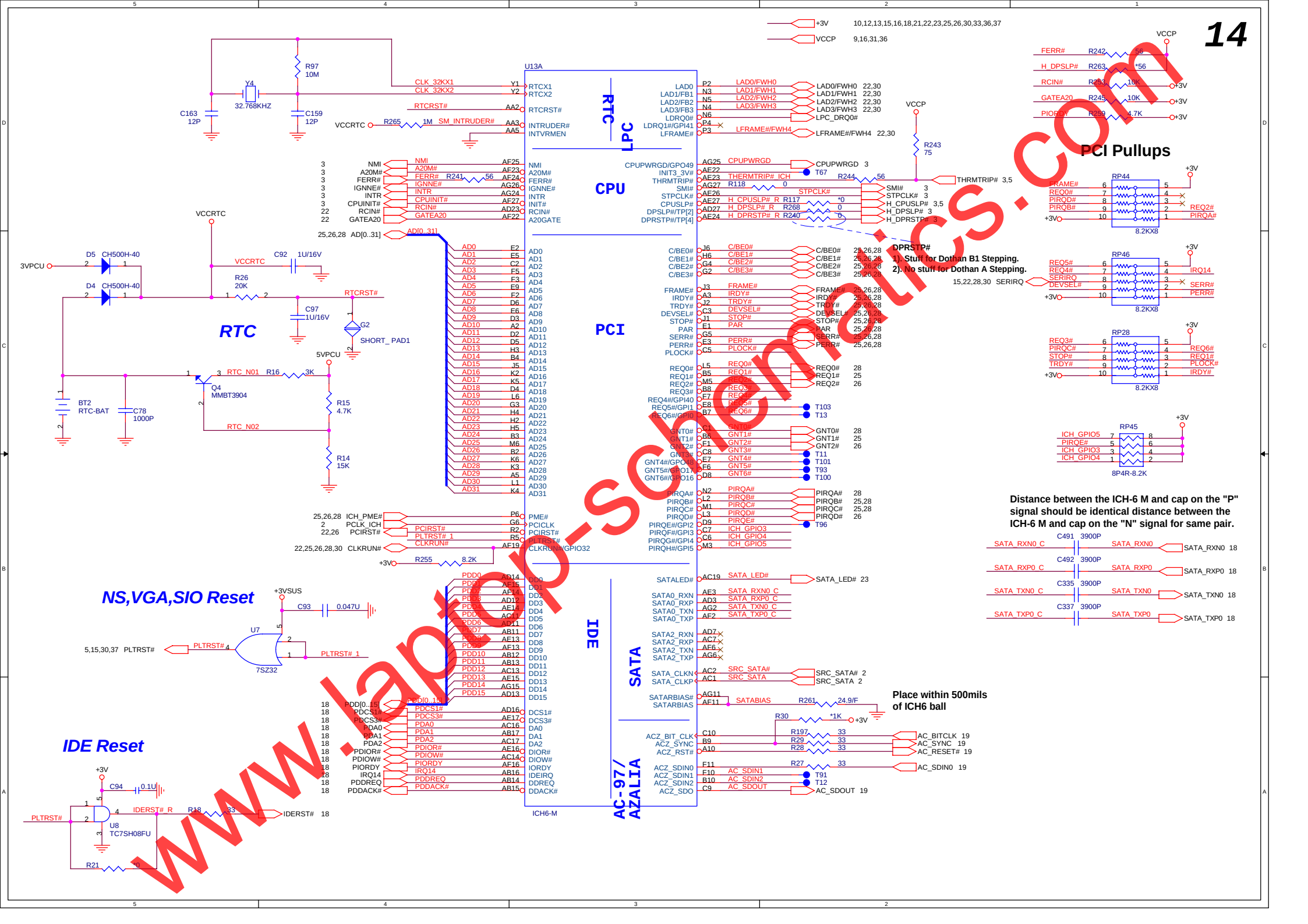
RTC
CPU
PCI
IDE
SATA
AC-97/ AZALIA
LPC
ICH6-M
U13A
RTC
NS,VGA,SIO Reset
IDE Reset
PCI Pullups
DPRSTP#
1). Stuff for Dothan B1 Stepping.
2). No stuff for Dothan A Stepping.
Place within 500mils of ICH6 ball
Distance between the ICH-6 M and cap on the "P" signal should be identical distance between the ICH-6 M and cap on the "N" signal for same pair.
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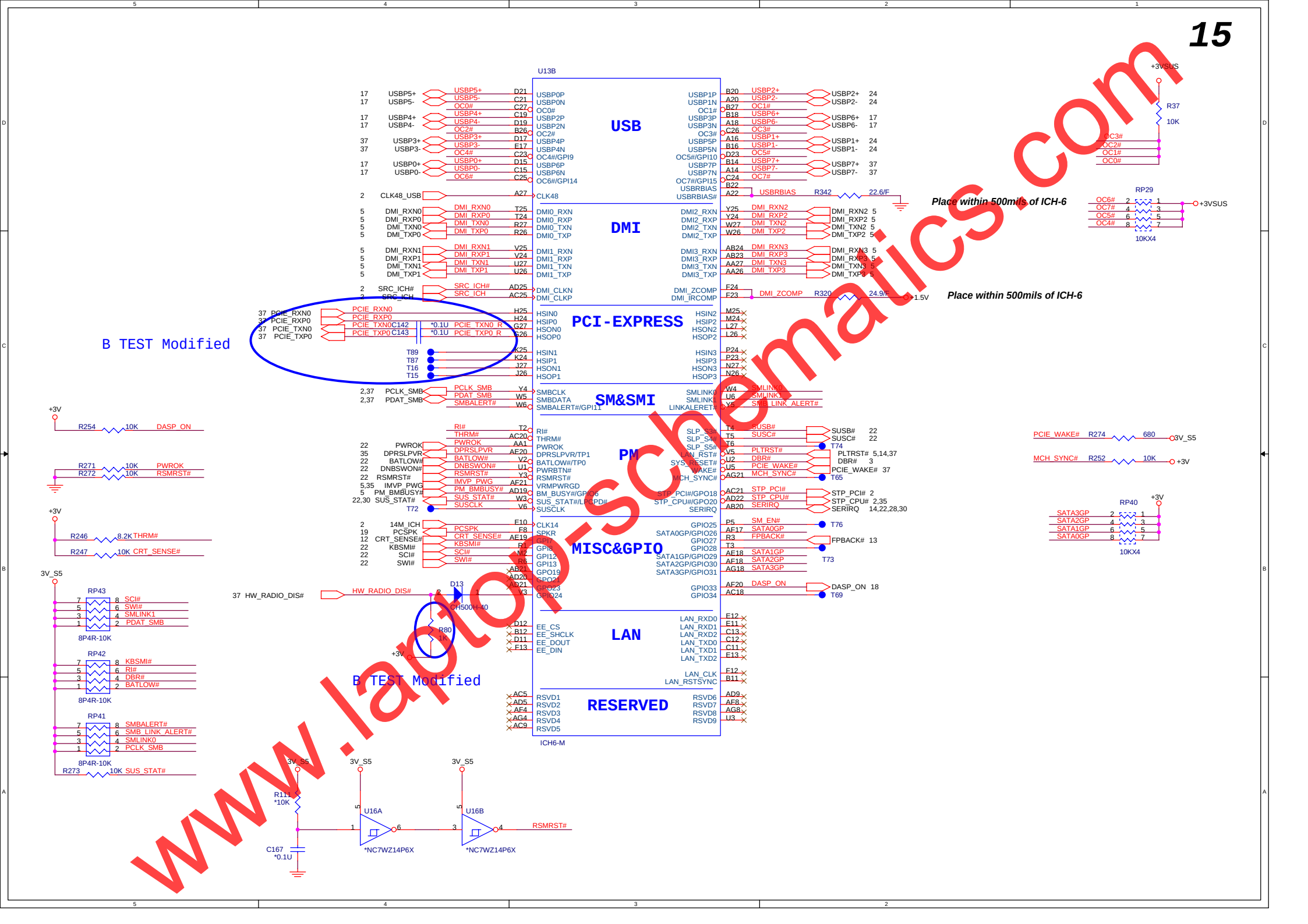
U13B
USB
DMI
PCI-EXPRESS
SM&SMI
PM
MISC&GPIO
LAN
RESERVED
ICH6-M
B TEST Modified
B TEST Modified
Place within 500mils of ICH-6
Place within 500mils of ICH-6
RP43
RP42
RP41
RP40
RP29
10KX4
8P4R-10K
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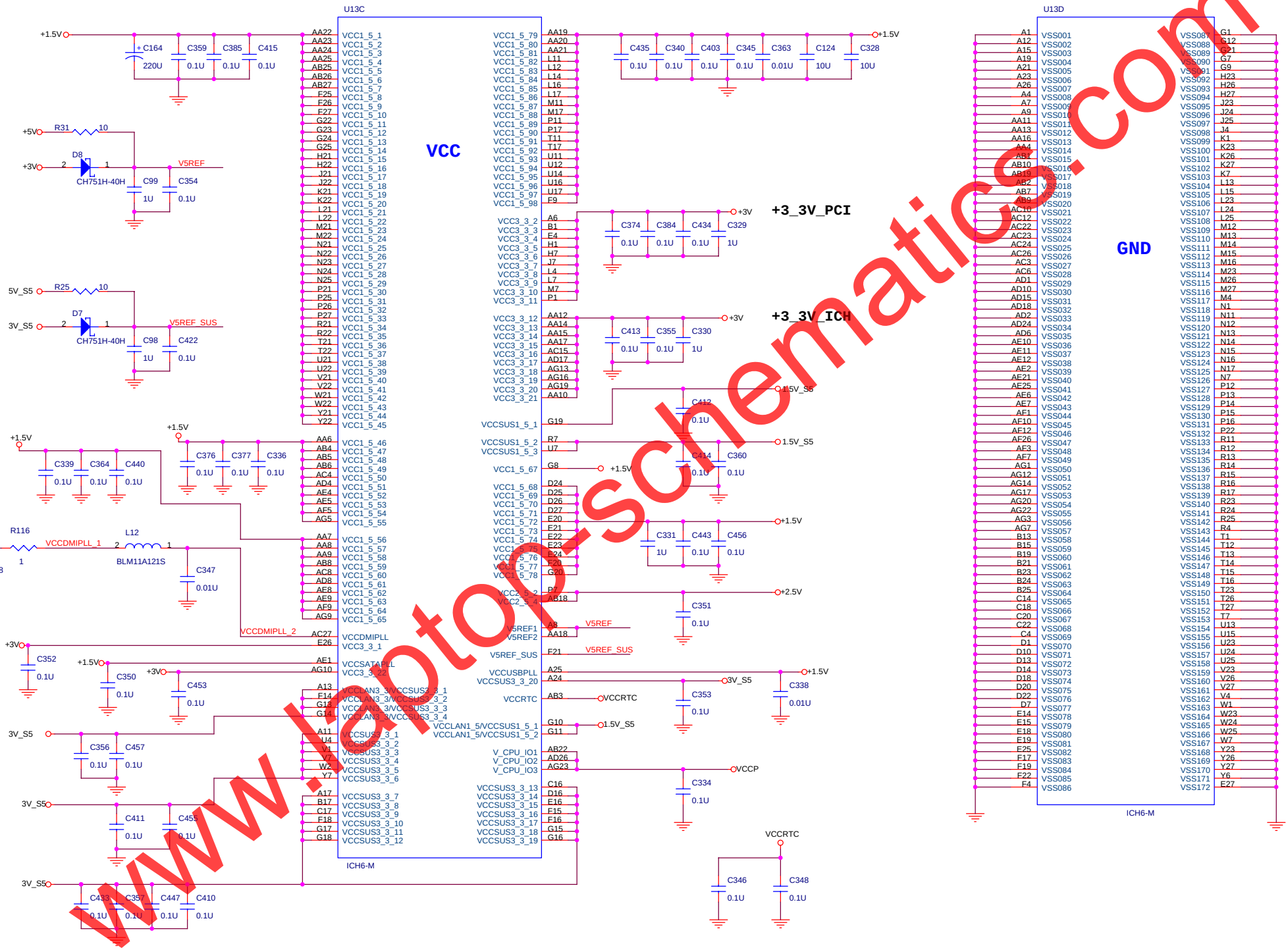

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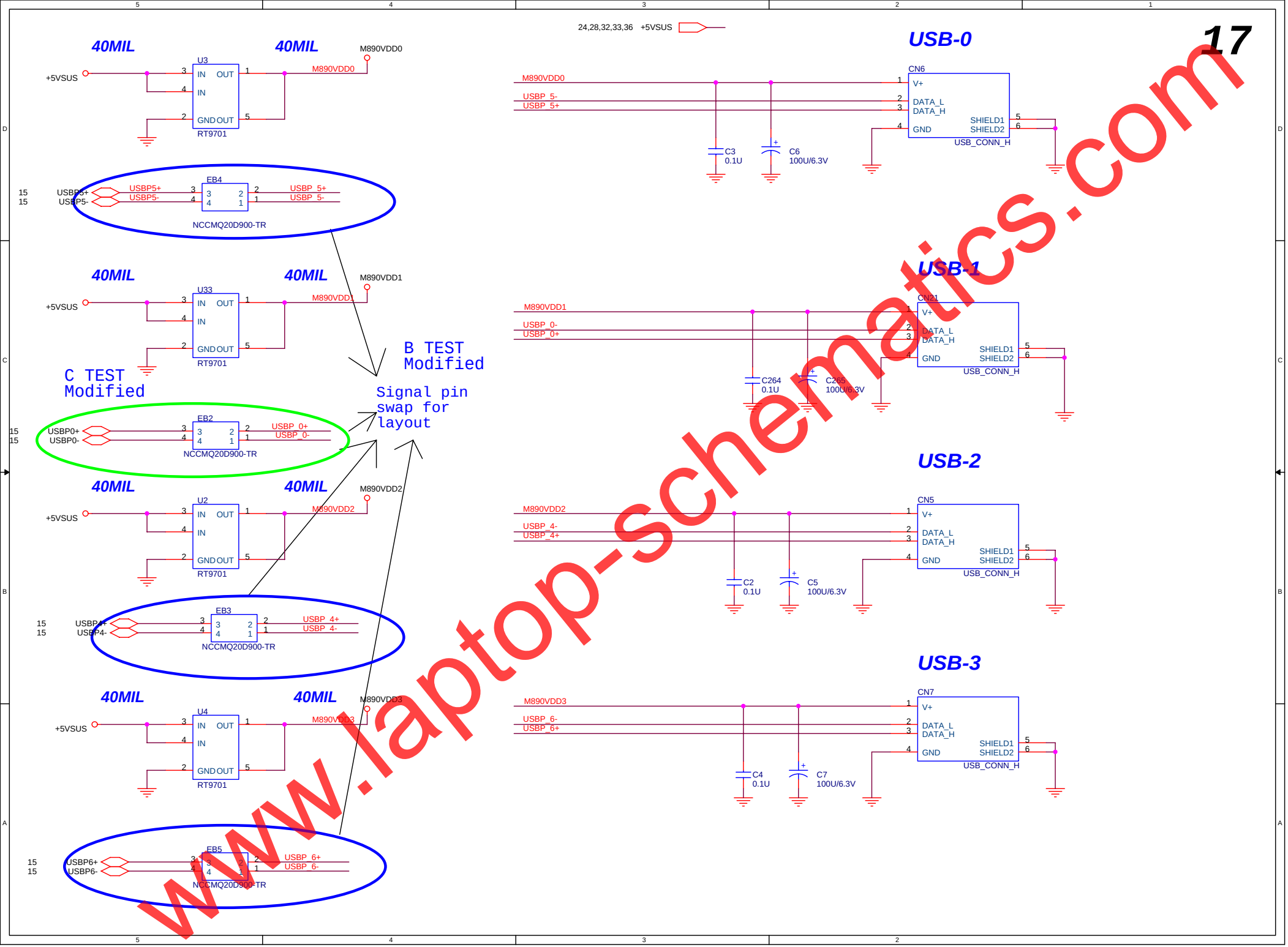

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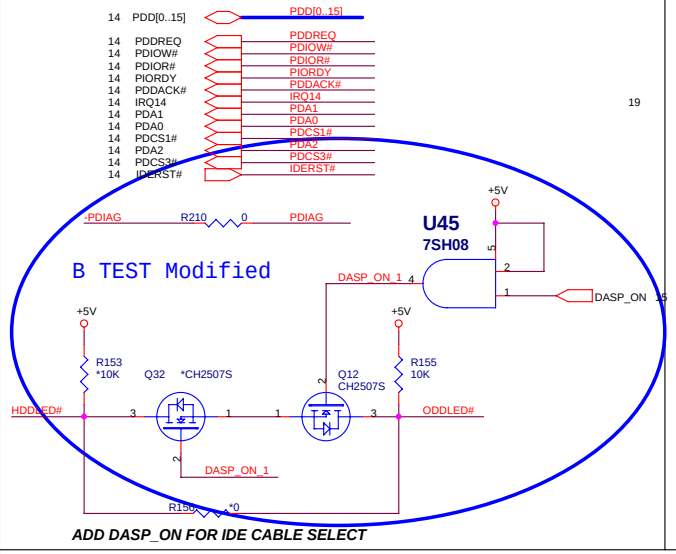

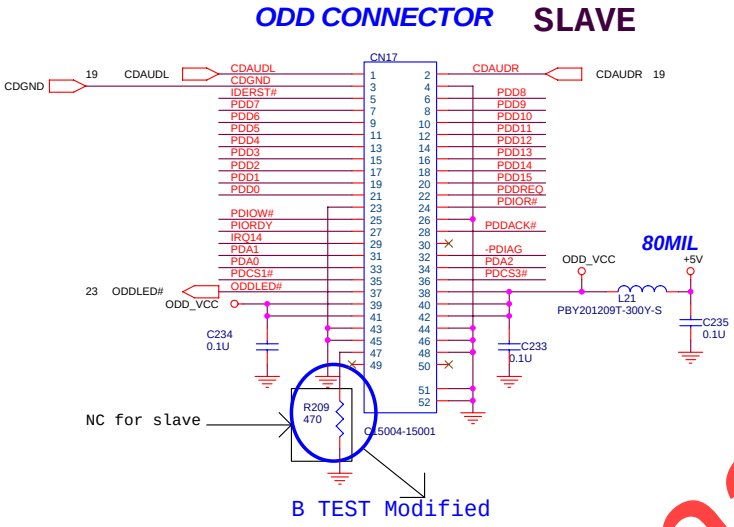

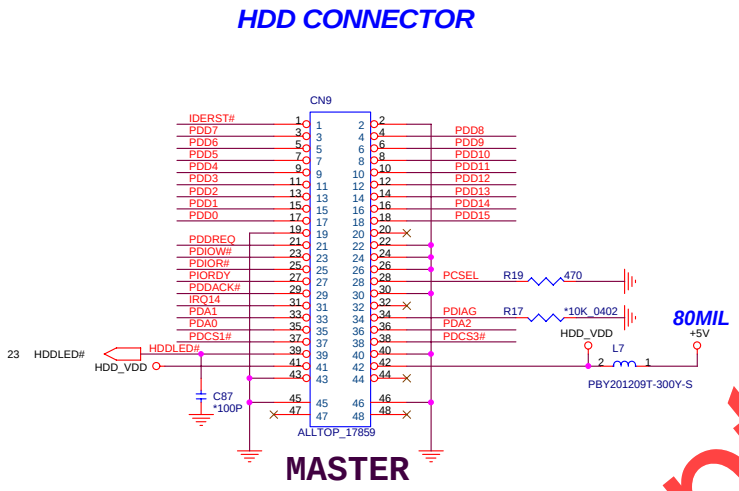

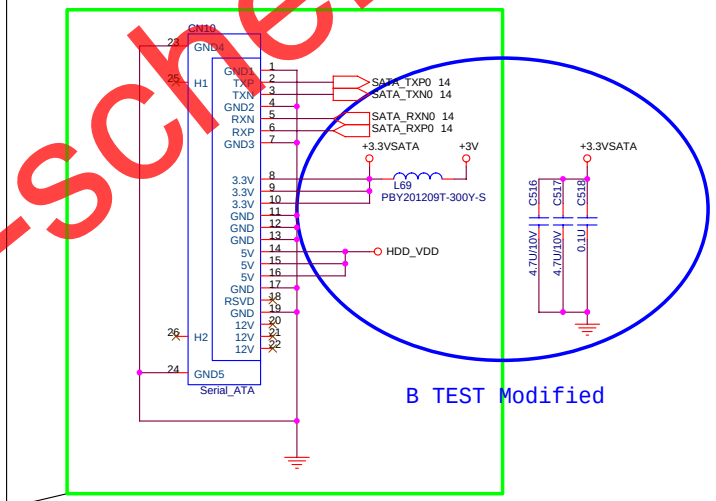

C TEST modified (footprint change)

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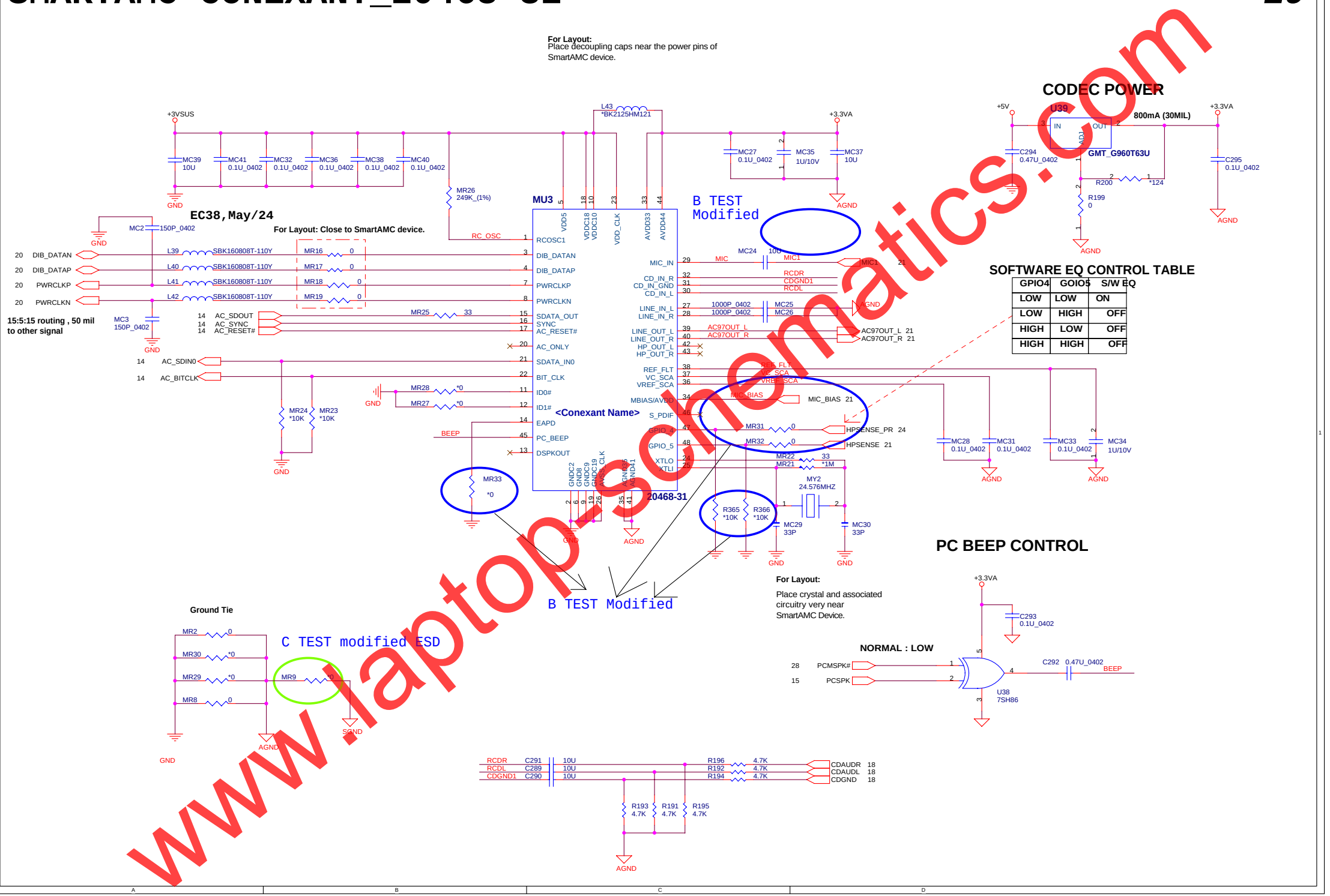

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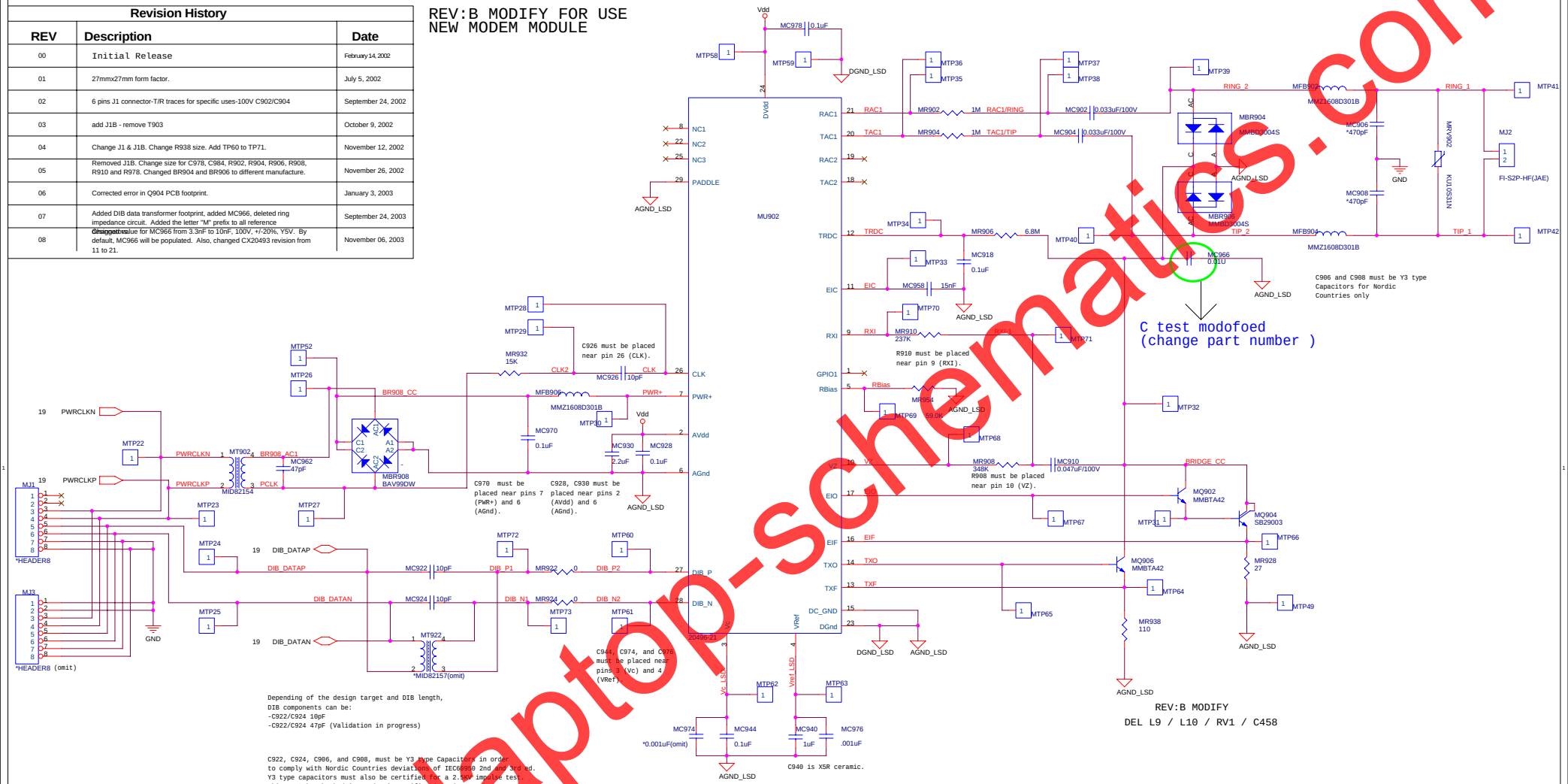
SMARTDAA_20493-21

Revision History		
REV	Description	Date
00	Initial Release	February 14, 2002
01	27mmx27mm form factor.	July 5, 2002
02	6 pins J1 connector-T/R traces for specific uses-100V C902/C904	September 24, 2002
03	add J1B - remove T903	October 9, 2002
04	Change J1 & J1B. Change R938 size. Add TP60 to TP71.	November 12, 2002
05	Removed J1B. Change size for C978, C984, R902, R904, R906, R908, R910 and R978. Changed BR904 and BR906 to different manufacture.	November 26, 2002
06	Corrected error in Q904 PCB footprint.	January 3, 2003
07	Added DIB data transformer footprint, added MC966, deleted ring impedance circuit. Added the letter "M" prefix to all reference designators.	September 24, 2003
08	Changed value for MC966 from 3.3nF to 10nF, 100V, +/-20%, Y5V. By default, MC966 will be populated. Also, changed CX20493 revision from 11 to 21.	November 06, 2003

REV:B MODIFY FOR USE
NEW MODEM MODULE



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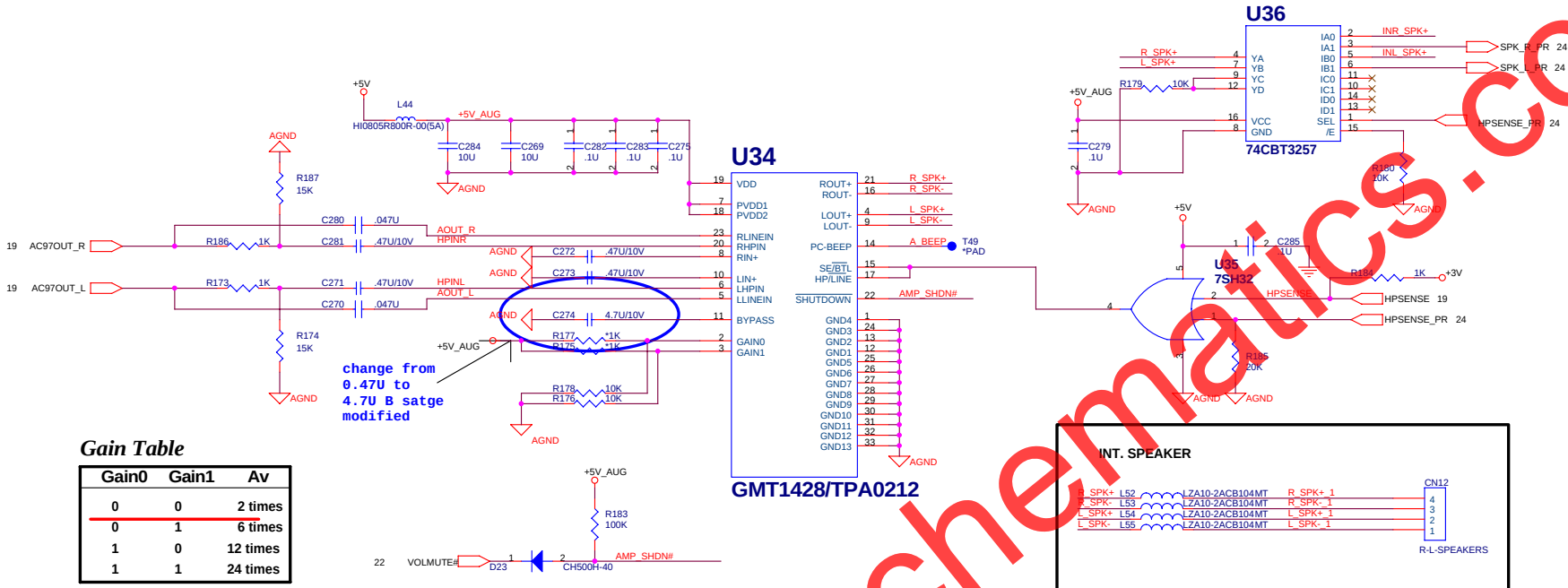
AUDIO AMPLIFIER-GMT_1428

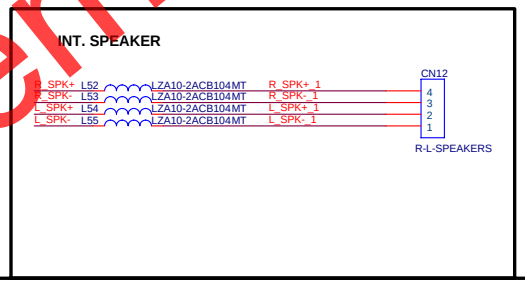
+5V 12,13,16,18,19,22,23,24,25,30,33,36

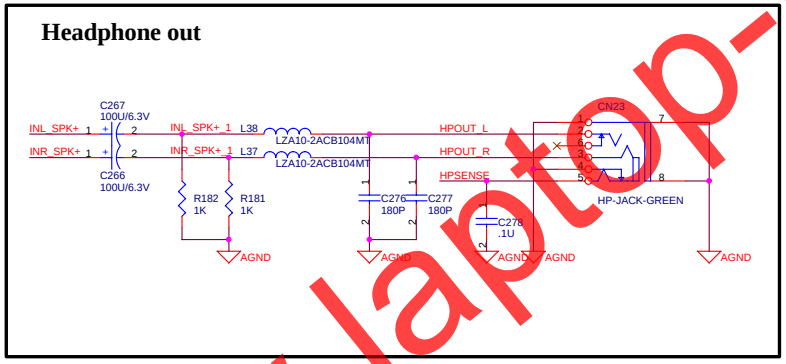
To Port-Replicator

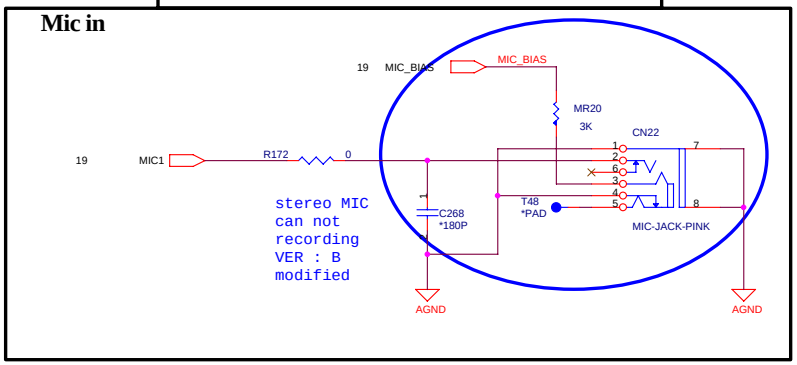


Gain Table

Gain0	Gain1	Av
0	0	2 times
0	1	6 times
1	0	12 times
1	1	24 times







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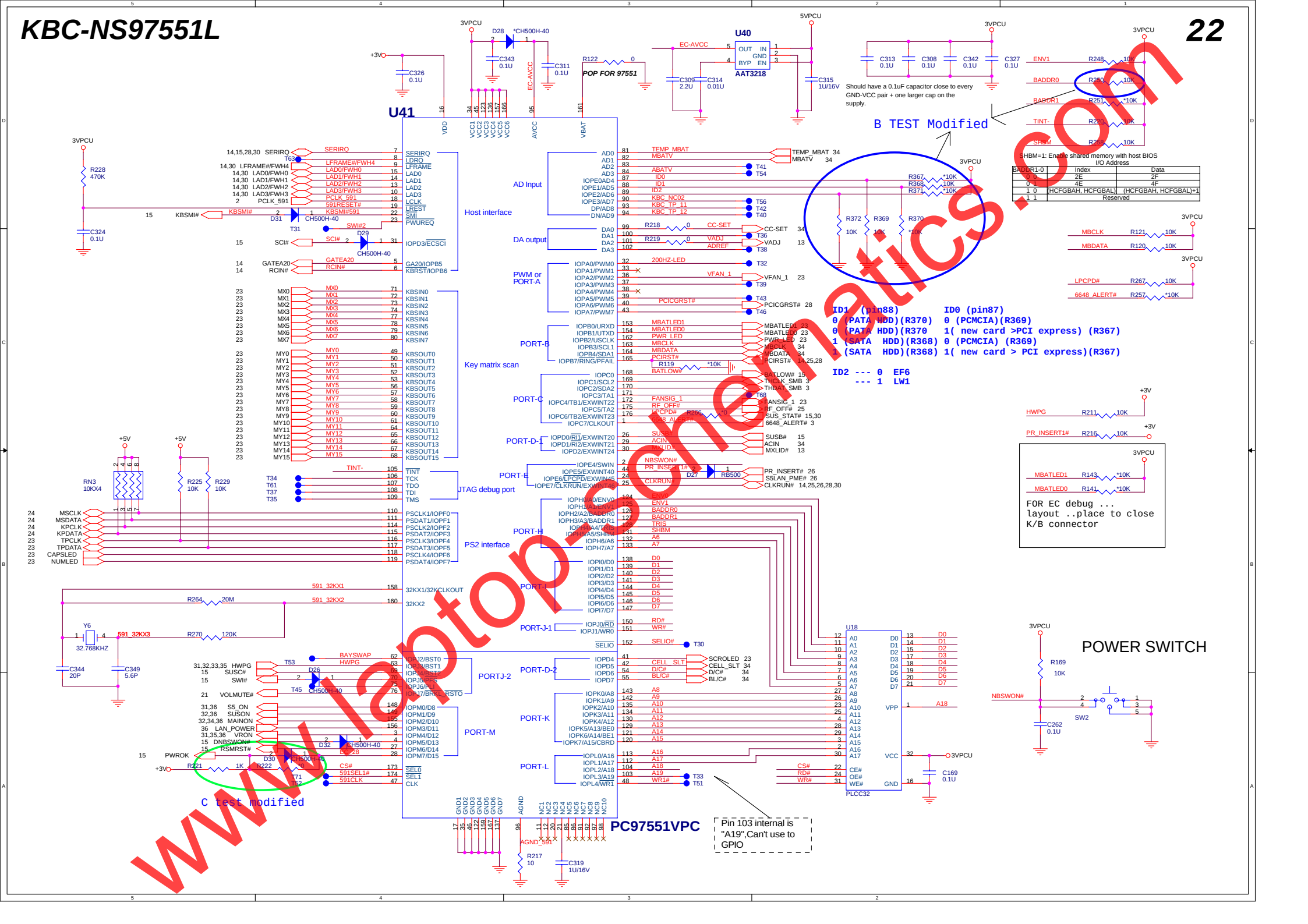

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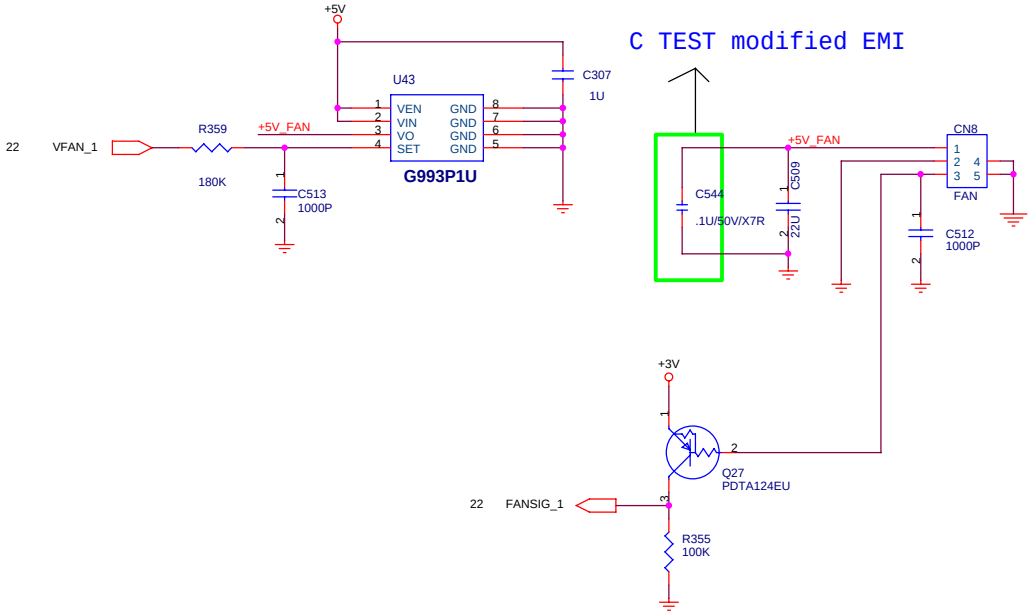
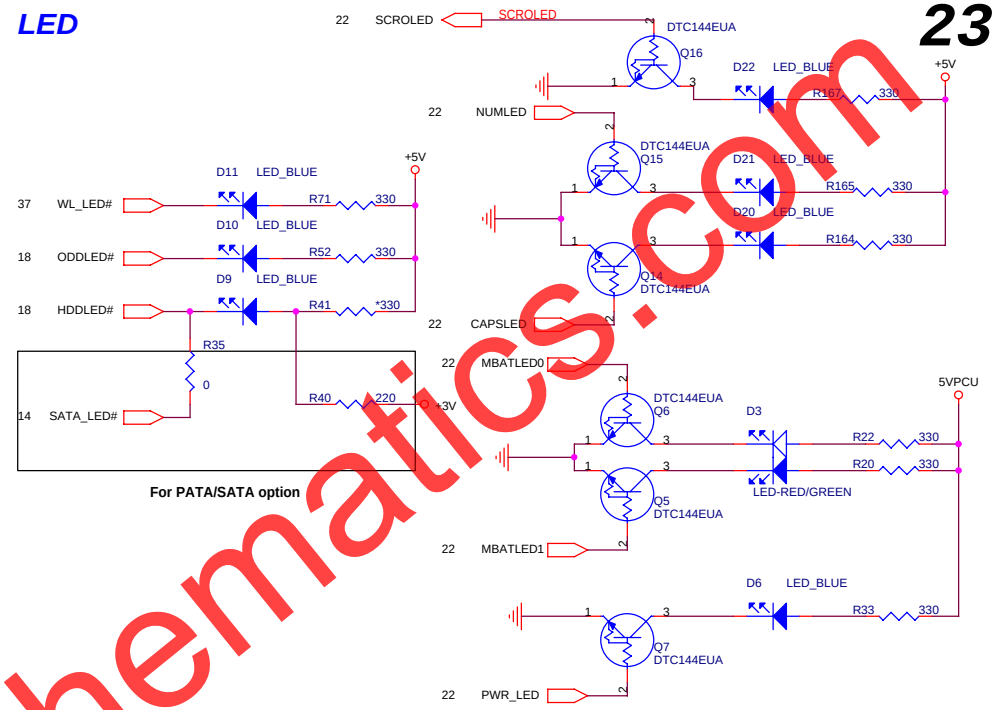
1st FAN OUT CONNECTOR



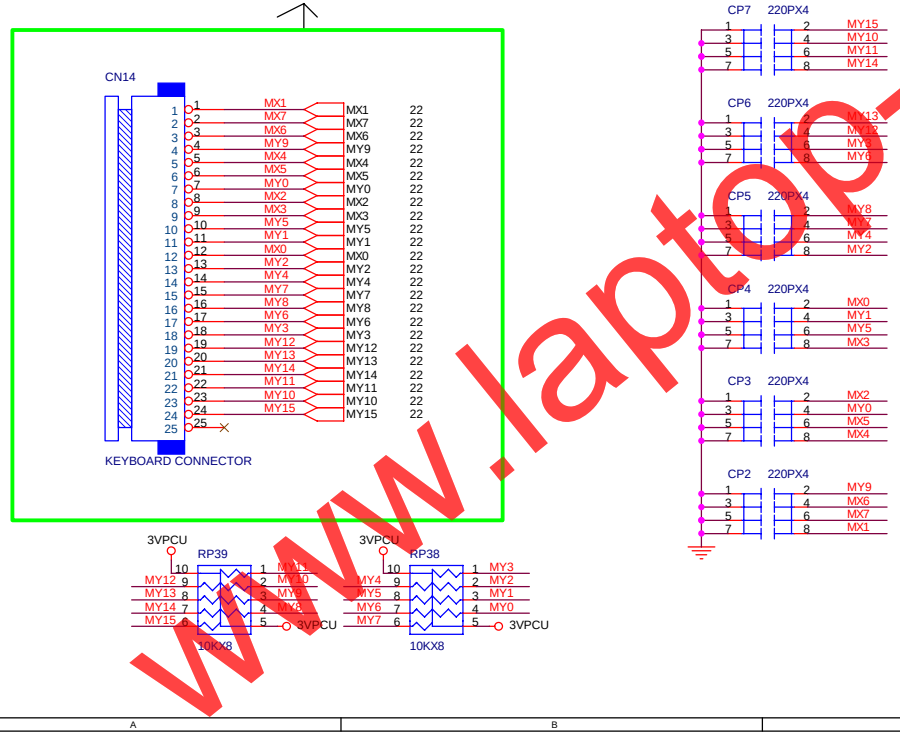
C TEST modified EMI

LED

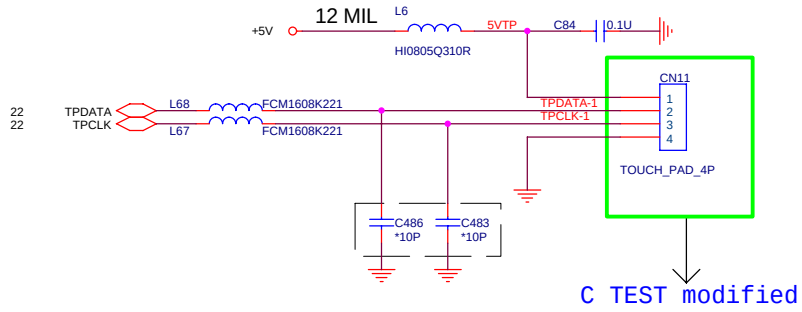


KEYBOARD

C TEST modified



TOUCHPAD



C TEST modified

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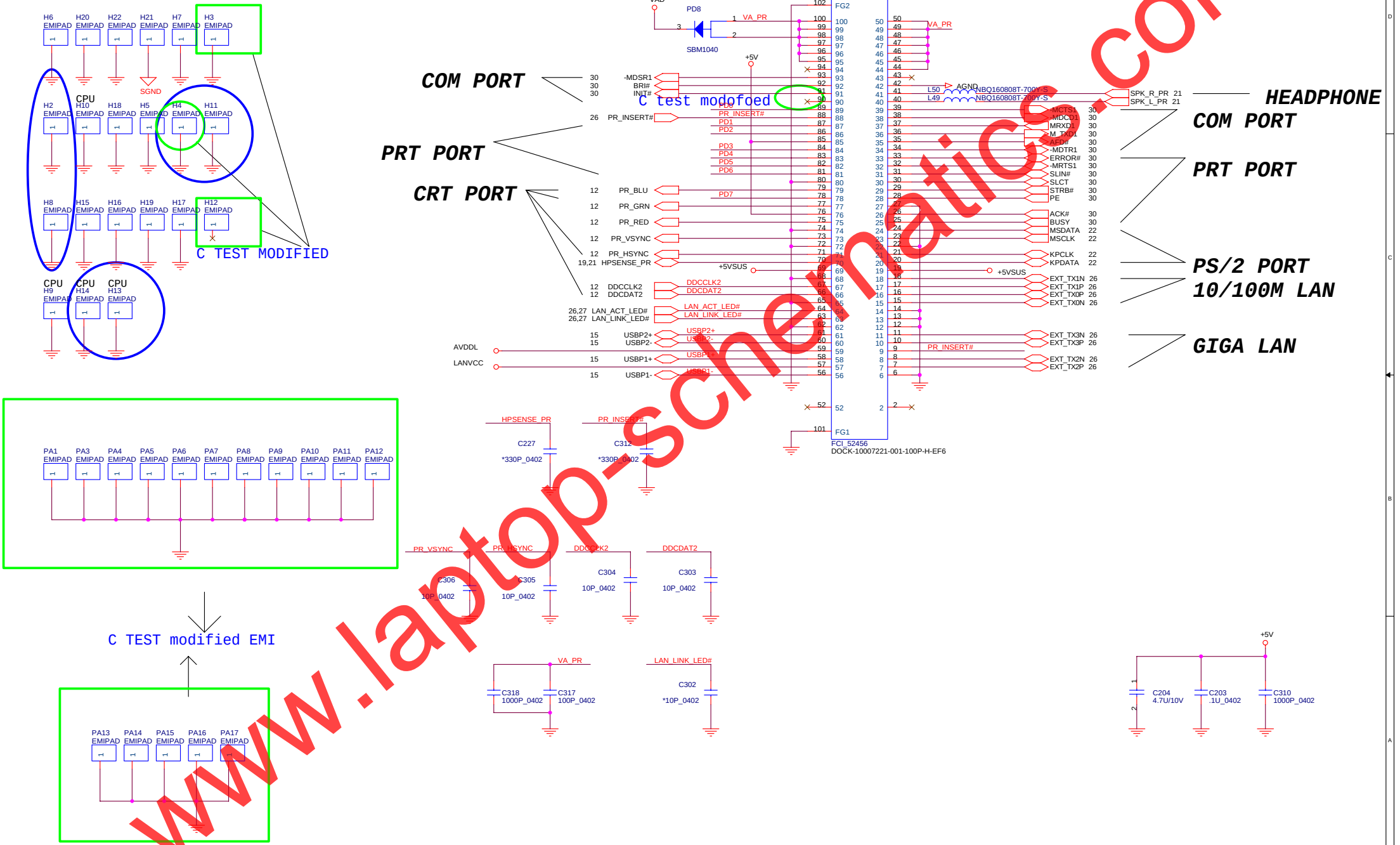
PORT REPLICATOR



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+5V 12,13,16,18,19,21,22,23,24,30,33,36

+3V 2,3,8,10,12,13,14,15,16,18,21,22,23,26,30,33,35,36,37

AD[0..31] AD[0..31] 14,26,28

TYPE III MINI PCI SOCKET

CN26 — MINI-PCI

C TEST modified

FOR 80 PORT USED
VER : B modified

FOR 80 PORT USED
VER: B modified

C TEST modified

change value and footprint 100K 0402 to 1M 0603

U19 7SH08

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LAN SWITCH

SEL	FUNCTION(COM)
LOW	IN_B1
HIGH	IN_B2

B STAGE MODIFIED

PI3L301

SWITCH

For 8110SB

LANVCC on/off will control by EC.

For 8100CL

50mils

LANVCC

RTL8110S(B)/8100C

8bit/16bit

AT93C46-10SC-2.7

B STAGE MODIFIED

	RTL8100C(L) _10/100M	RTL8110SB(L) _ 1G
VDD33	3.3VD 26,41,56,71,84,94,107	3.3VD 26,41,56,71,84,94,107
AVDDL	3.3VA 3,7,16,20	2.5VA 3,7,16,20
DVDD	2.5VD 32,54,78,99	1.2VD 24,32,46,54,64,78,99,110,116,126
AVDD25	2.5VA 12	3.3VA 12
AVDDH	NC	3.3VA 10,120
Pin 127	5.6k (1%) pull-low	2.49k (1%) pull-low

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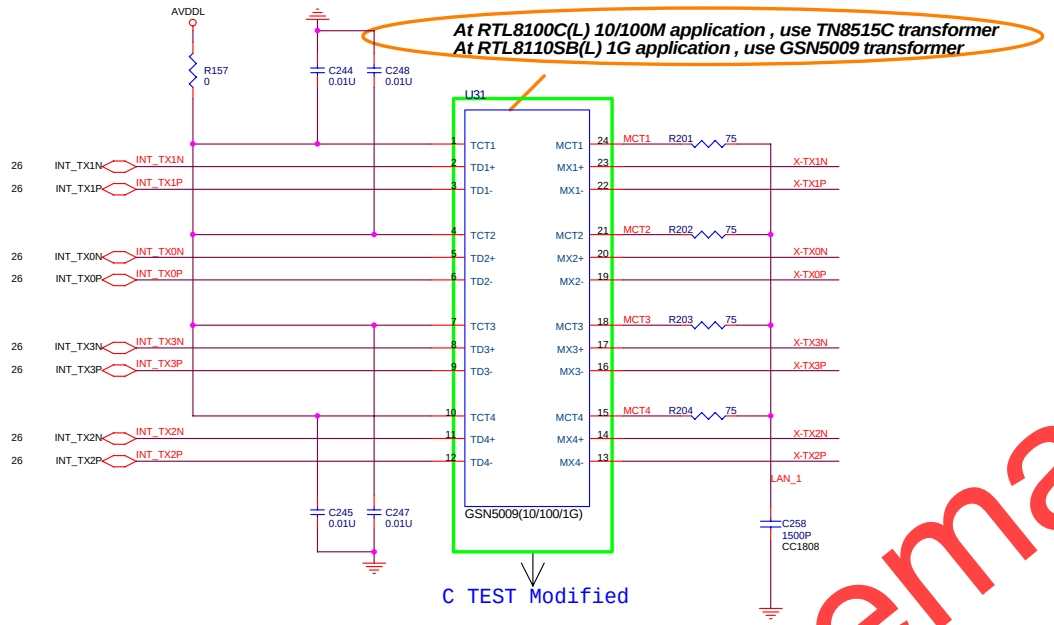
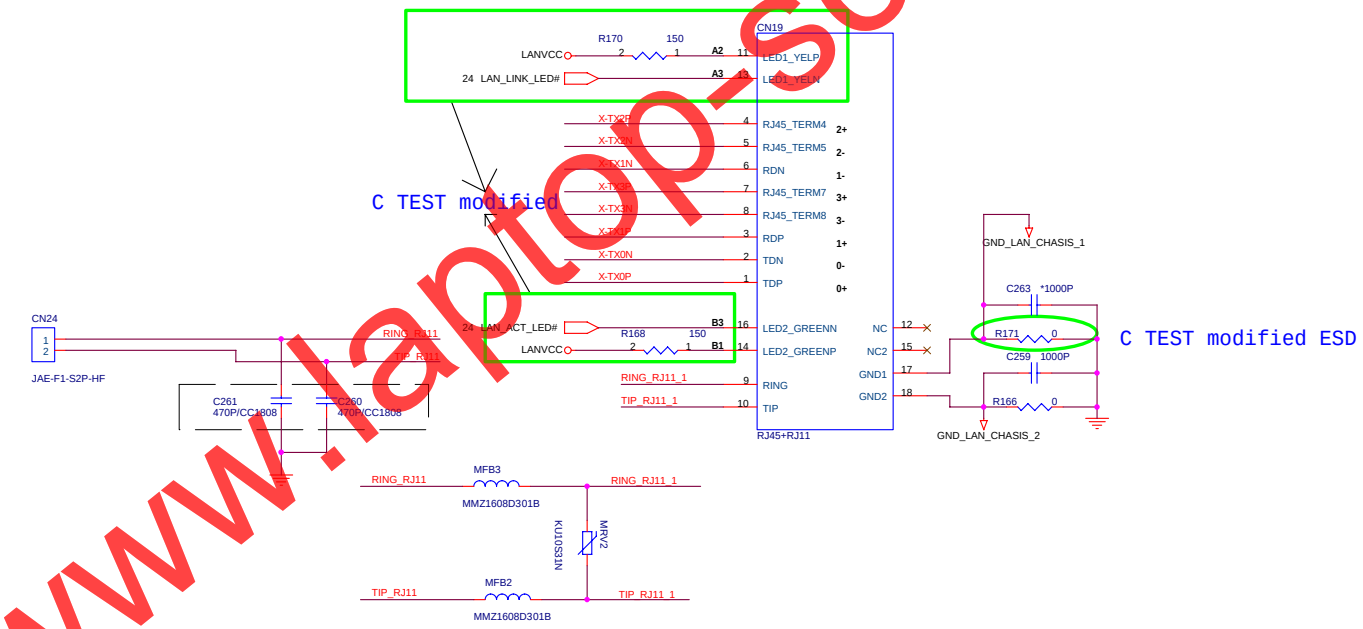


RJ45 and RJ11 CONNECTOR



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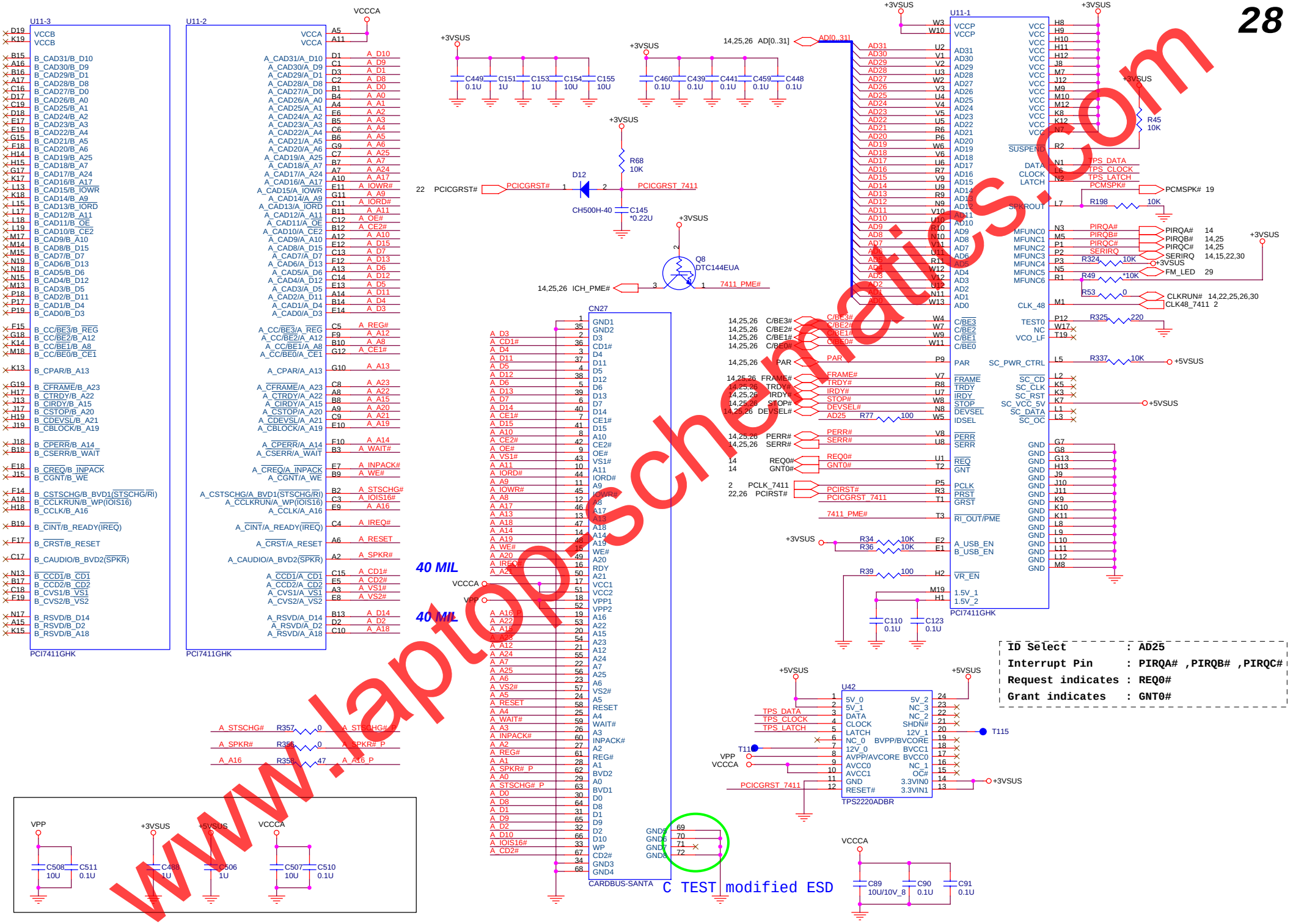

ID Select : AD25
Interrupt Pin : PIRQA# ,PIRQB# ,PIRQC#
Request indicates : REQ0#
Grant indicates : GNT0#

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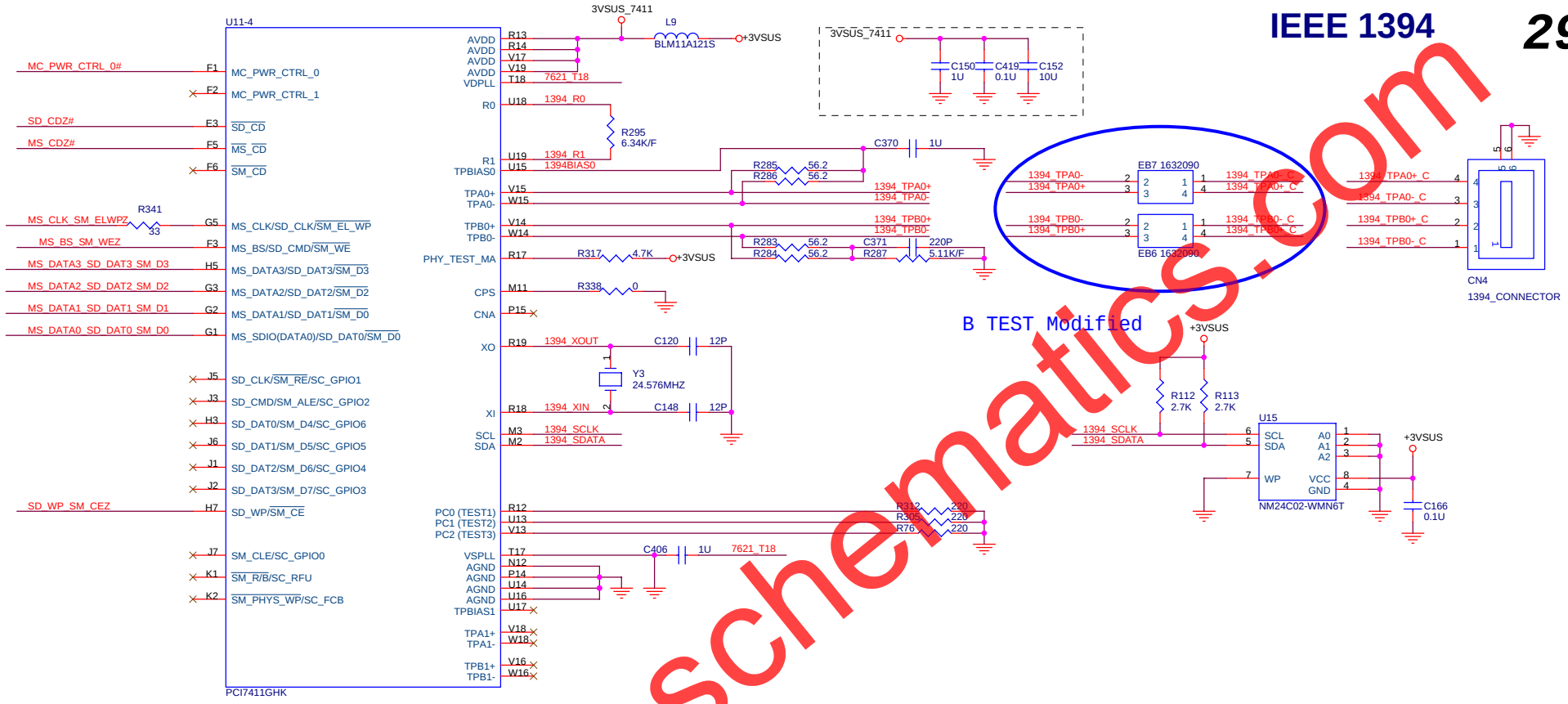

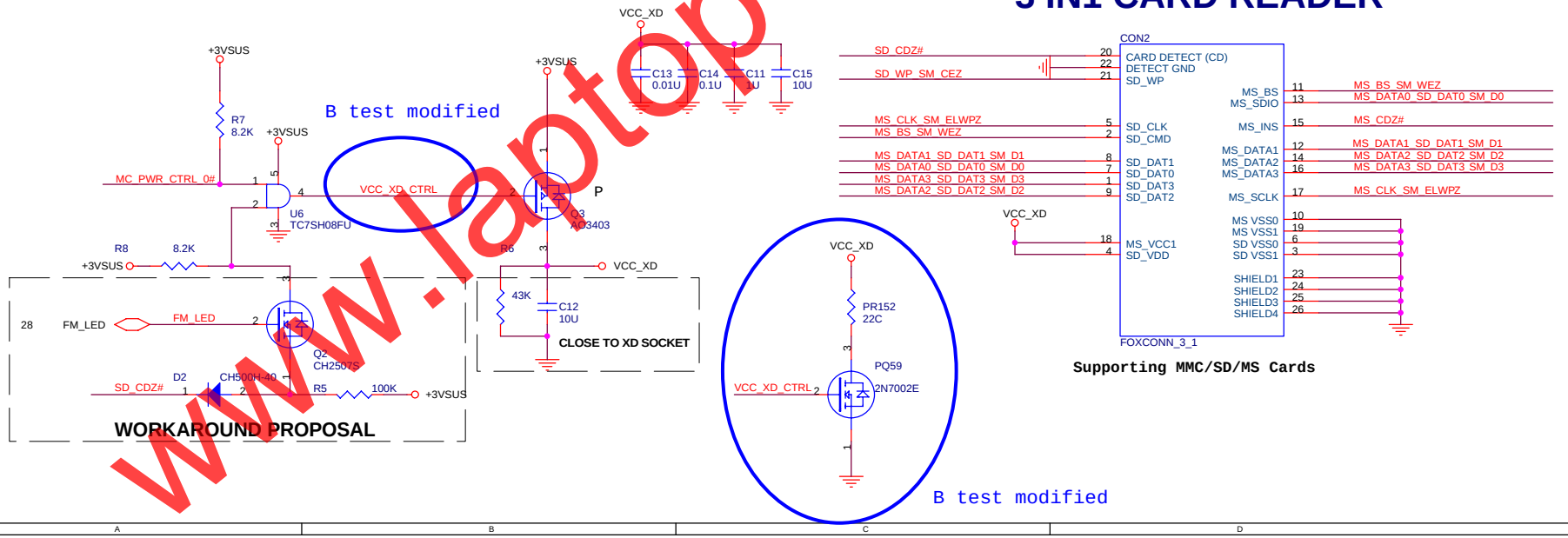

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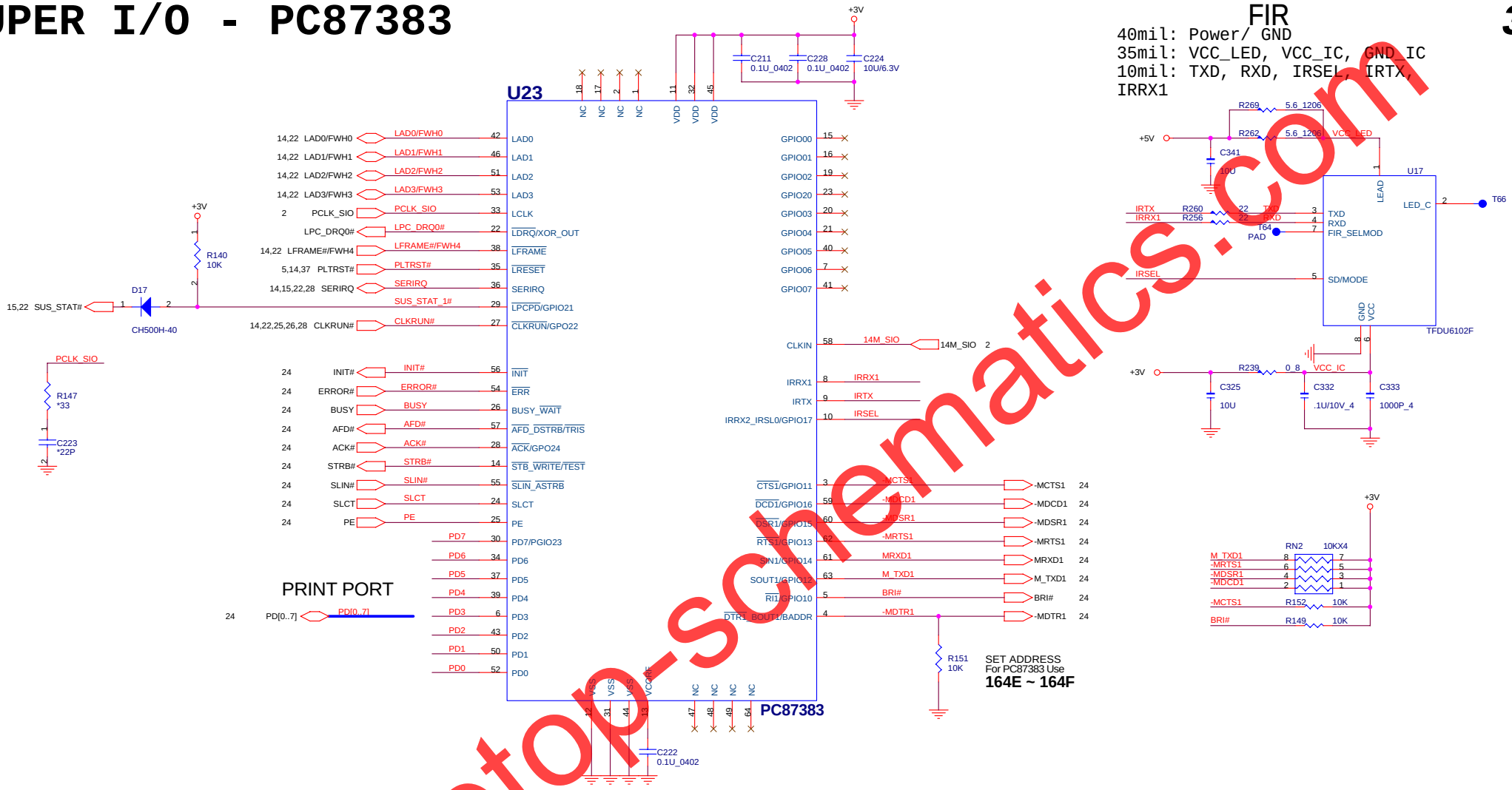
SUPER I/O - PC87383

FIR

40mil: Power/ GND
35mil: VCC_LED, VCC_IC, GND_IC
10mil: TXD, RXD, IRSEL, IRTX, IRRX1



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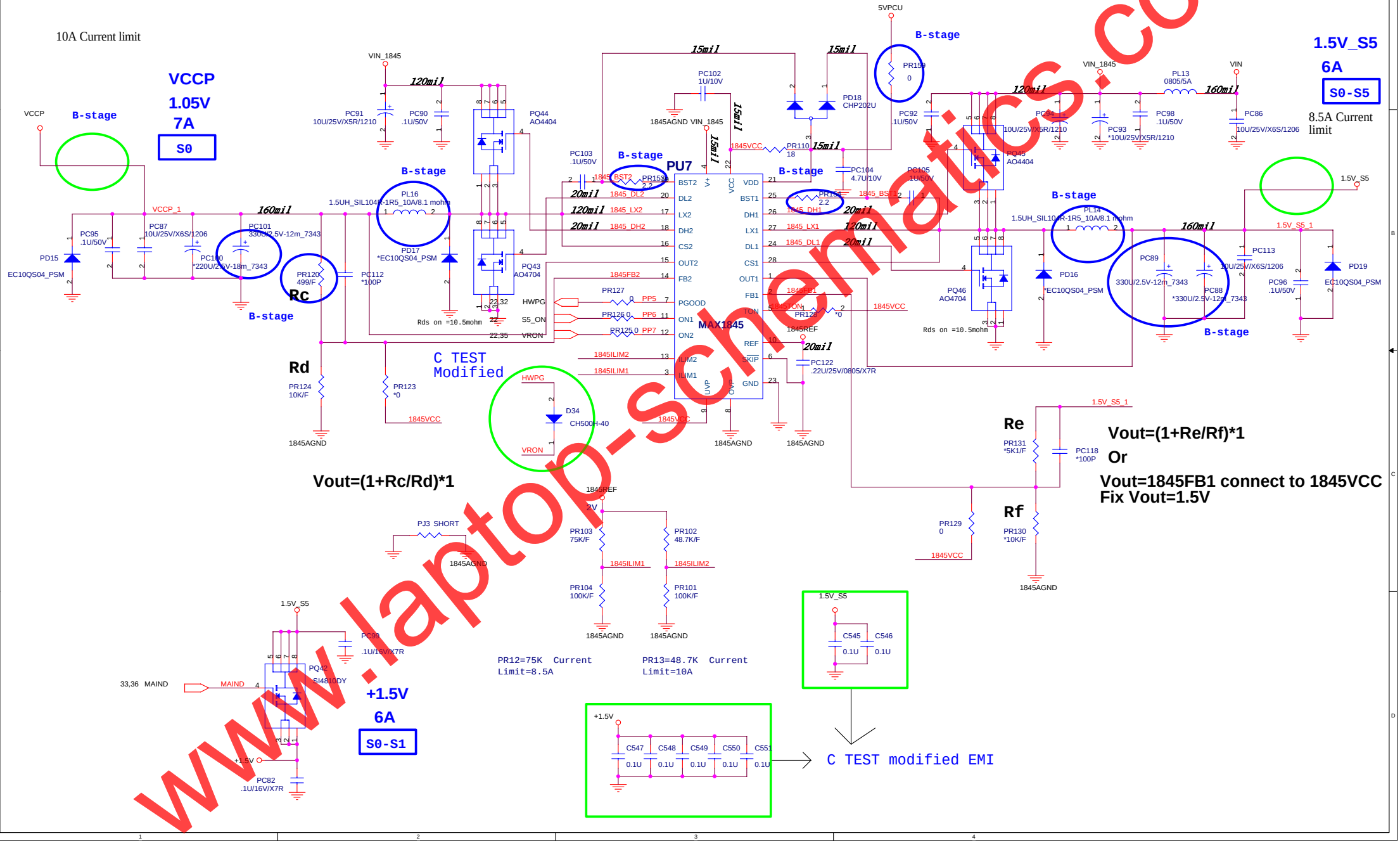
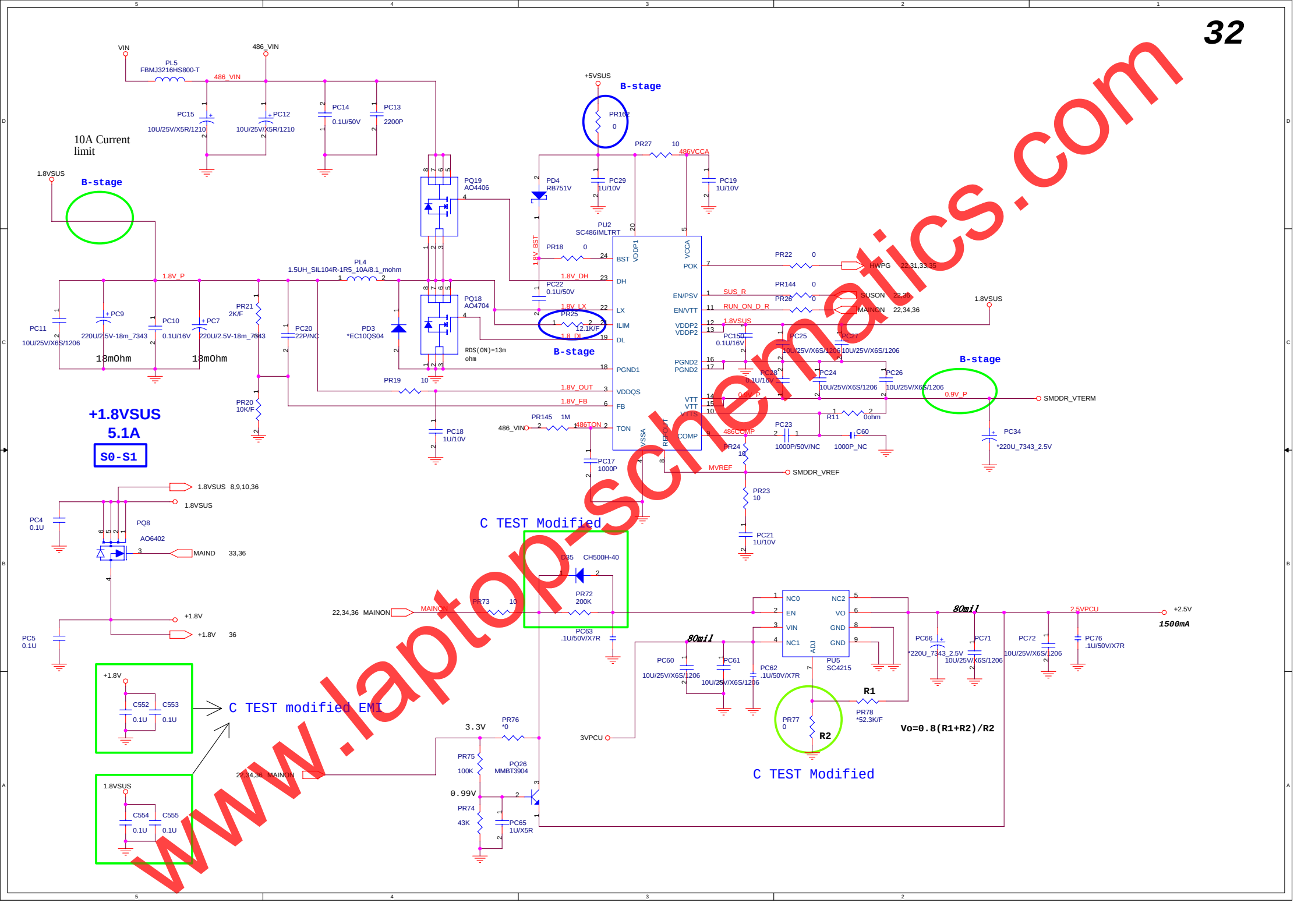

10A Current limit
VCCP
1.05V
7A
S0
B-stage
1.5V_S5
6A
S0-S5
8.5A Current limit
PU7
MAX1845
C TEST Modified
Rc
Rd
Re
Rf
Vout=(1+Rc/Rd)*1
Vout=(1+Re/Rf)*1
Or
Vout=1845FB1 connect to 1845VCC
Fix Vout=1.5V
+1.5V
6A
S0-S1
PR12=75K Current Limit=8.5A
PR13=48.7K Current Limit=10A
C TEST modified EMI
www.laptop-schematics.com



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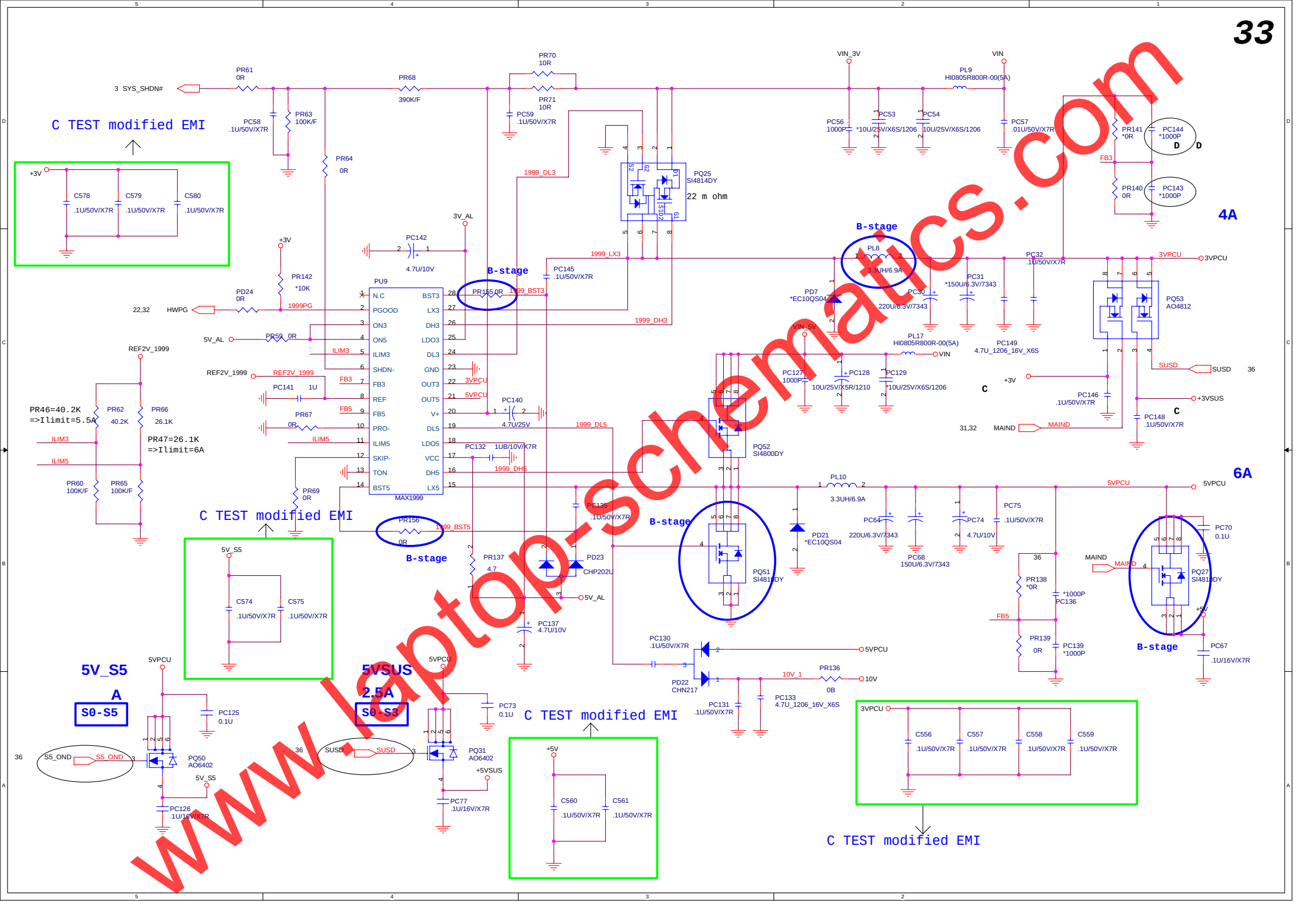

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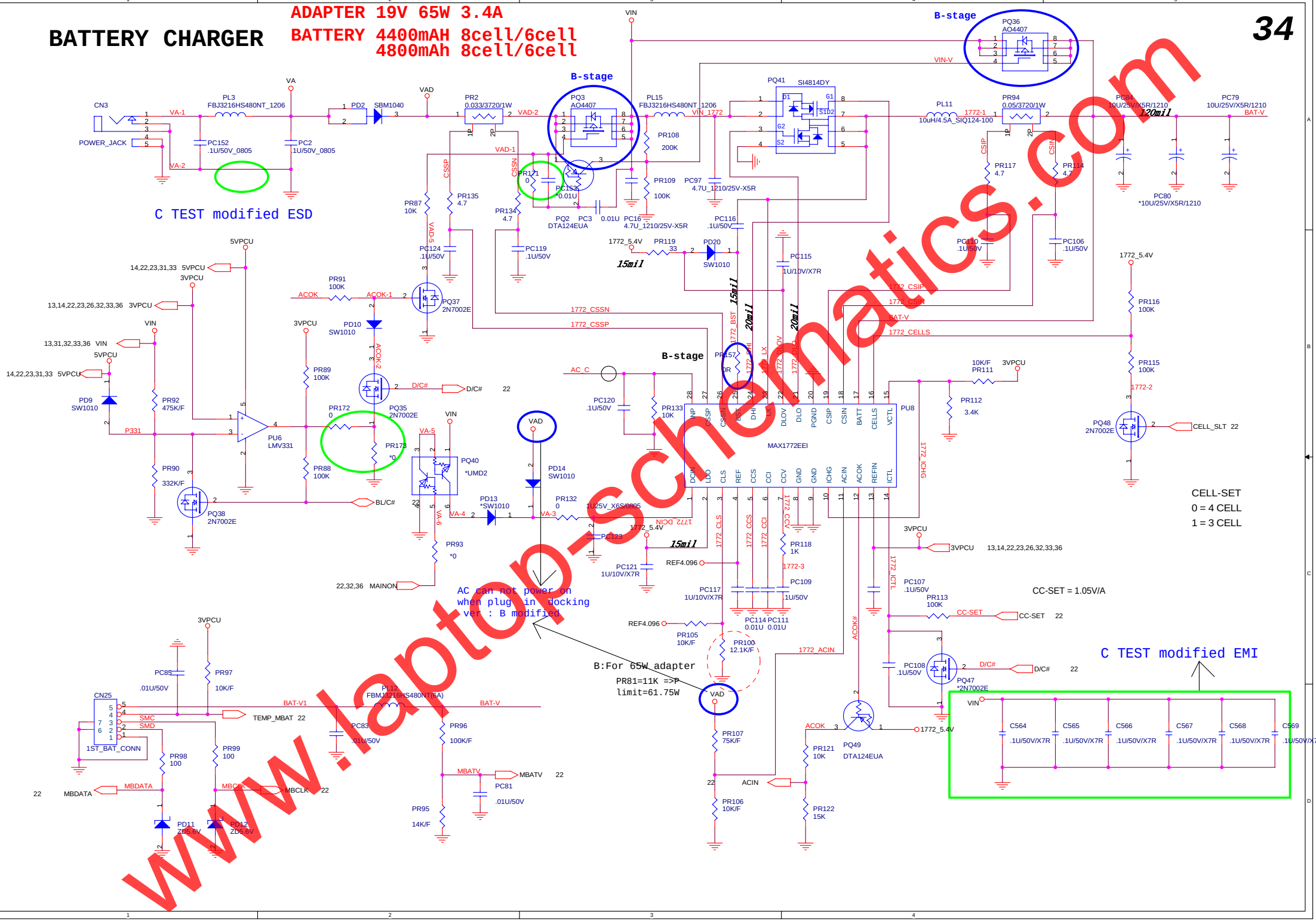
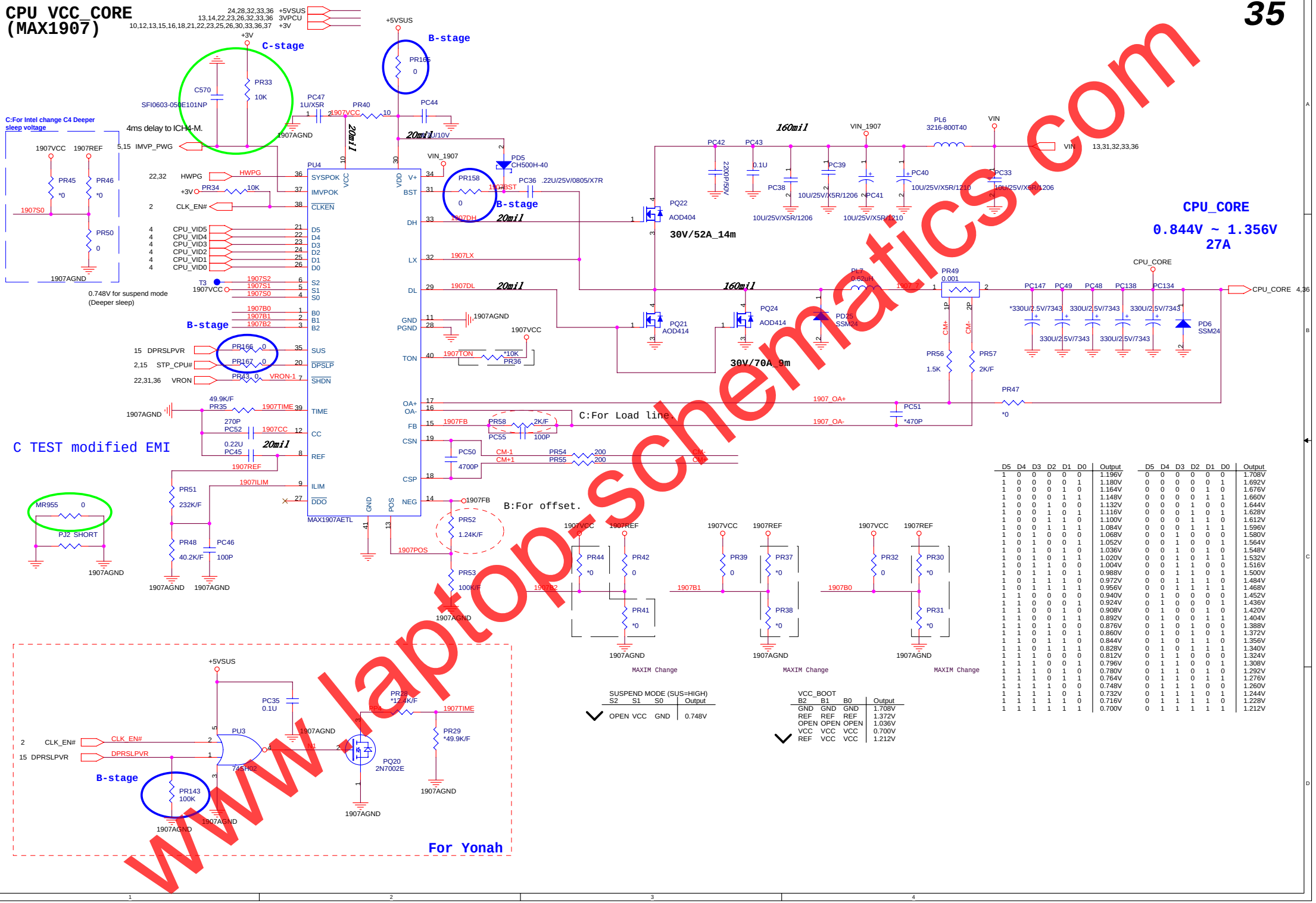
BATTERY CHARGER

ADAPTER 19V 65W 3.4A
BATTERY 4400mAH 8cell/6cell
4800mAh 8cell/6cell



CPU_VCC_CORE (MAX1907)



D5	D4	D3	D2	D1	D0	Output
1	0	0	0	0	0	1.196V
1	0	0	0	0	1	1.180V
1	0	0	0	1	0	1.164V
1	0	0	0	1	1	1.148V
1	0	0	1	0	0	1.132V
1	0	0	1	0	1	1.116V
1	0	0	1	1	0	1.100V
1	0	0	1	1	1	1.084V
1	0	1	0	0	0	1.068V
1	0	1	0	0	1	1.052V
1	0	1	0	1	0	1.036V
1	0	1	0	1	1	1.020V
1	0	1	1	0	0	1.004V
1	0	1	1	0	1	0.988V
1	0	1	1	1	0	0.972V
1	0	1	1	1	1	0.956V
1	1	0	0	0	0	0.940V
1	1	0	0	0	1	0.924V
1	1	0	0	1	0	0.908V
1	1	0	0	1	1	0.892V
1	1	0	1	0	0	0.876V
1	1	0	1	0	1	0.860V
1	1	0	1	1	0	0.844V
1	1	0	1	1	1	0.828V
1	1	1	0	0	0	0.812V
1	1	1	0	0	1	0.796V
1	1	1	0	1	0	0.780V
1	1	1	0	1	1	0.764V
1	1	1	1	0	0	0.748V
1	1	1	1	0	1	0.732V
1	1	1	1	1	0	0.716V
1	1	1	1	1	1	0.700V

D5	D4	D3	D2	D1	D0	Output
0	0	0	0	0	0	1.708V
0	0	0	0	0	1	1.692V
0	0	0	0	1	0	1.676V
0	0	0	0	1	1	1.660V
0	0	0	1	0	0	1.644V
0	0	0	1	0	1	1.628V
0	0	0	1	1	0	1.612V
0	0	0	1	1	1	1.596V
0	0	1	0	0	0	1.580V
0	0	1	0	0	1	1.564V
0	0	1	0	1	0	1.548V
0	0	1	0	1	1	1.532V
0	0	1	1	0	0	1.516V
0	0	1	1	0	1	1.500V
0	0	1	1	1	0	1.484V
0	0	1	1	1	1	1.468V
0	1	0	0	0	0	1.452V
0	1	0	0	0	1	1.436V
0	1	0	0	1	0	1.420V
0	1	0	0	1	1	1.404V
0	1	0	1	0	0	1.388V
0	1	0	1	0	1	1.372V
0	1	0	1	1	0	1.356V
0	1	0	1	1	1	1.340V
0	1	1	0	0	0	1.324V
0	1	1	0	0	1	1.308V
0	1	1	0	1	0	1.292V
0	1	1	0	1	1	1.276V
0	1	1	1	0	0	1.260V
0	1	1	1	0	1	1.244V
0	1	1	1	1	0	1.228V
0	1	1	1	1	1	1.212V

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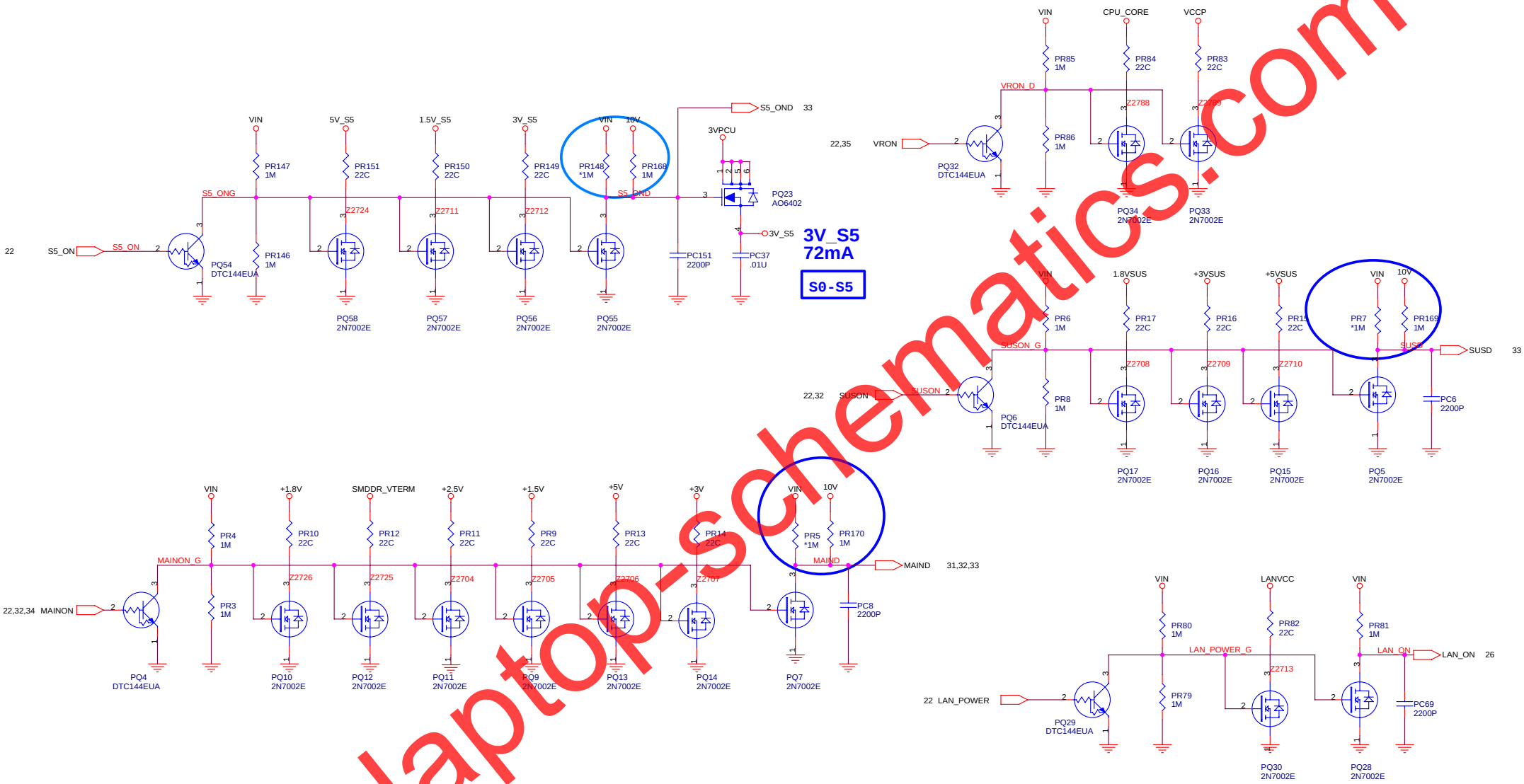

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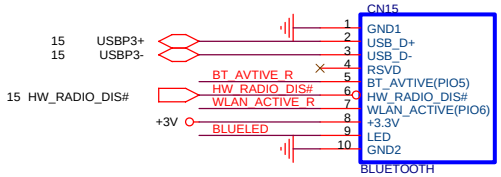
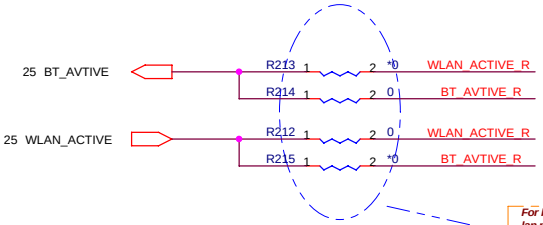
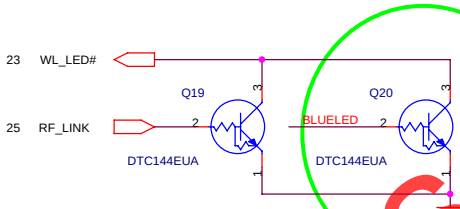
BLUETOOTH CONNECTOR



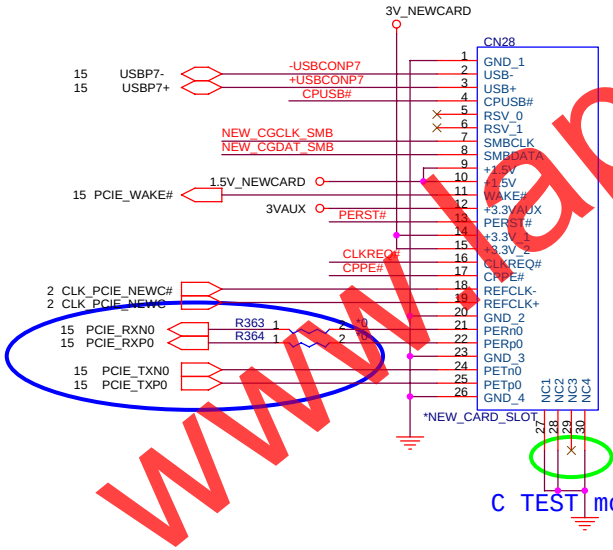


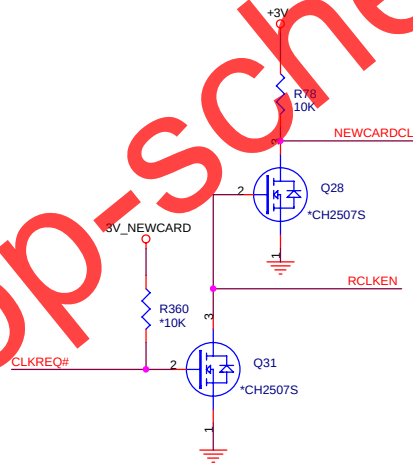
For bluetooth V1.2 spec option and combine intel wireless lan module use ,BC2-ext also provide the co-existence solution .

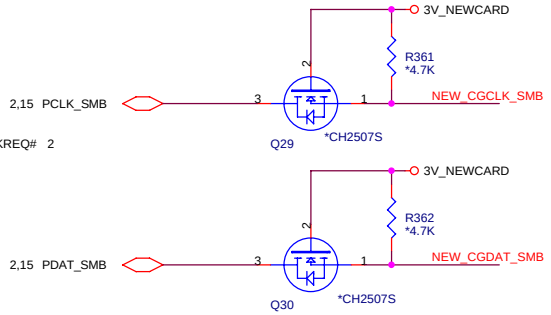


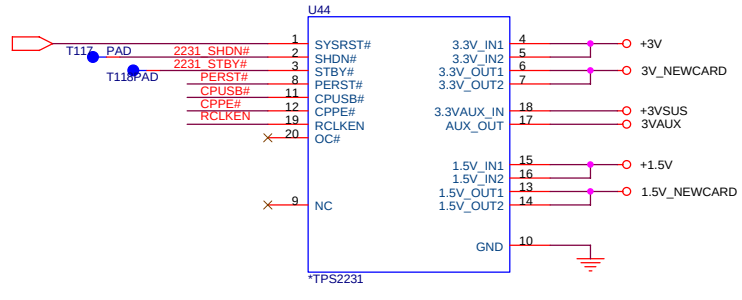
C TEST Modified

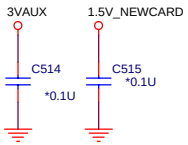
NEWCARD (PCIEXPRESS*1 + USB*1)











C TEST modified ESD

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